

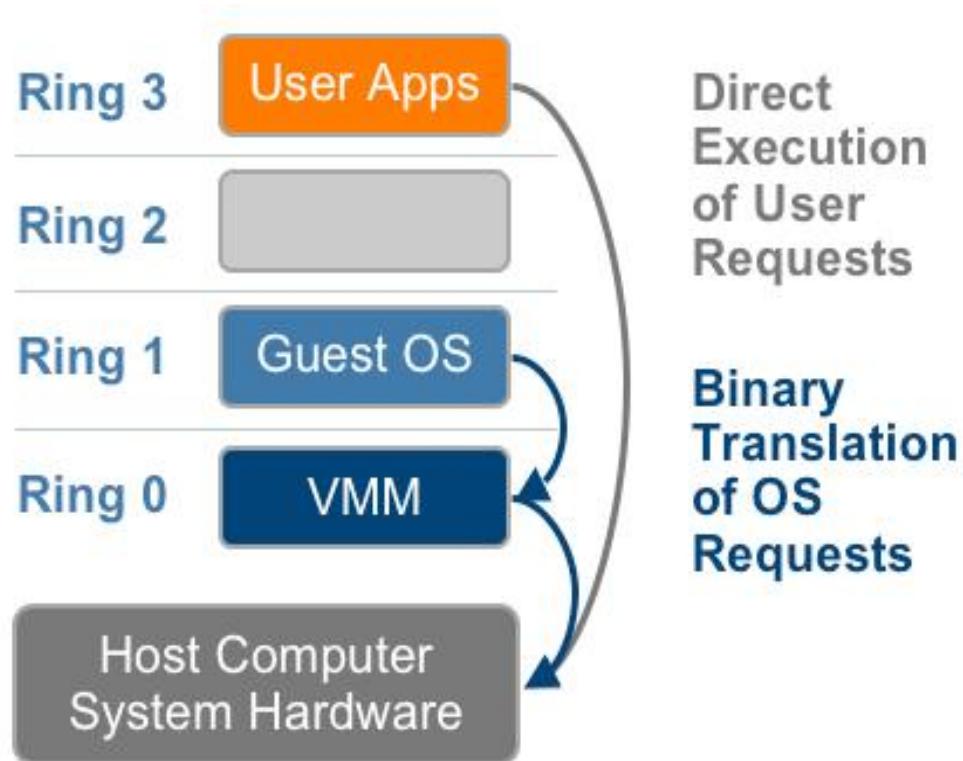
QEMU Binary Translation

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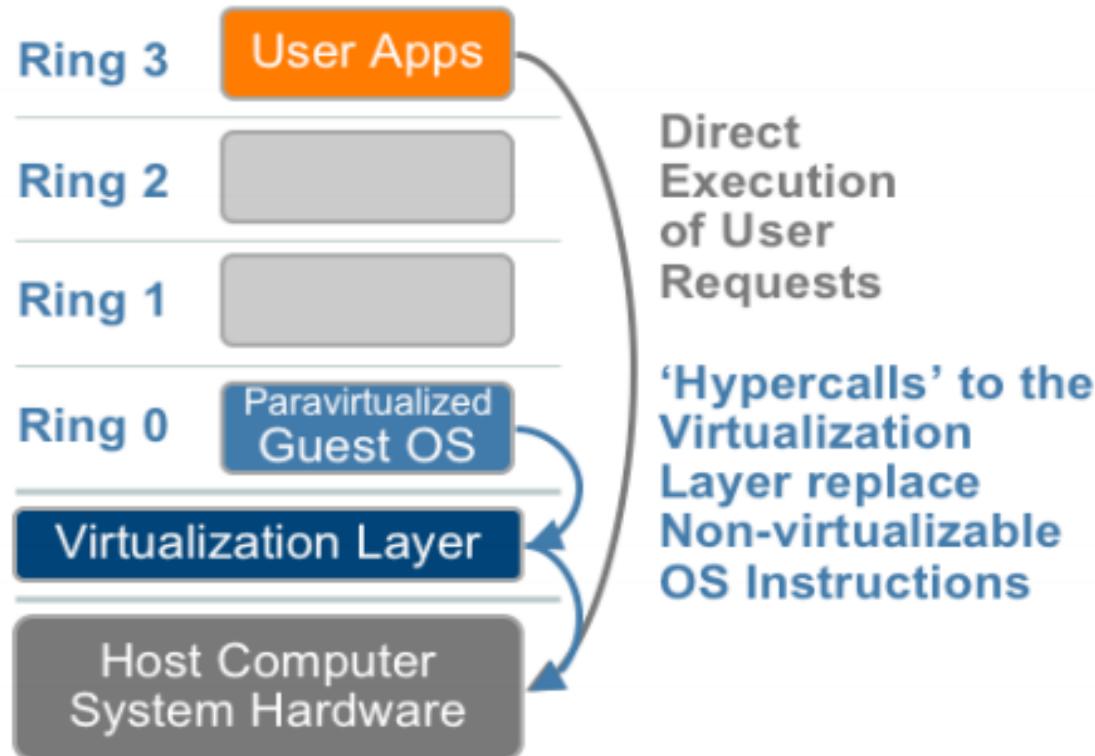
Virtualization Techniques

- **Full Virtualization using Binary Translation**



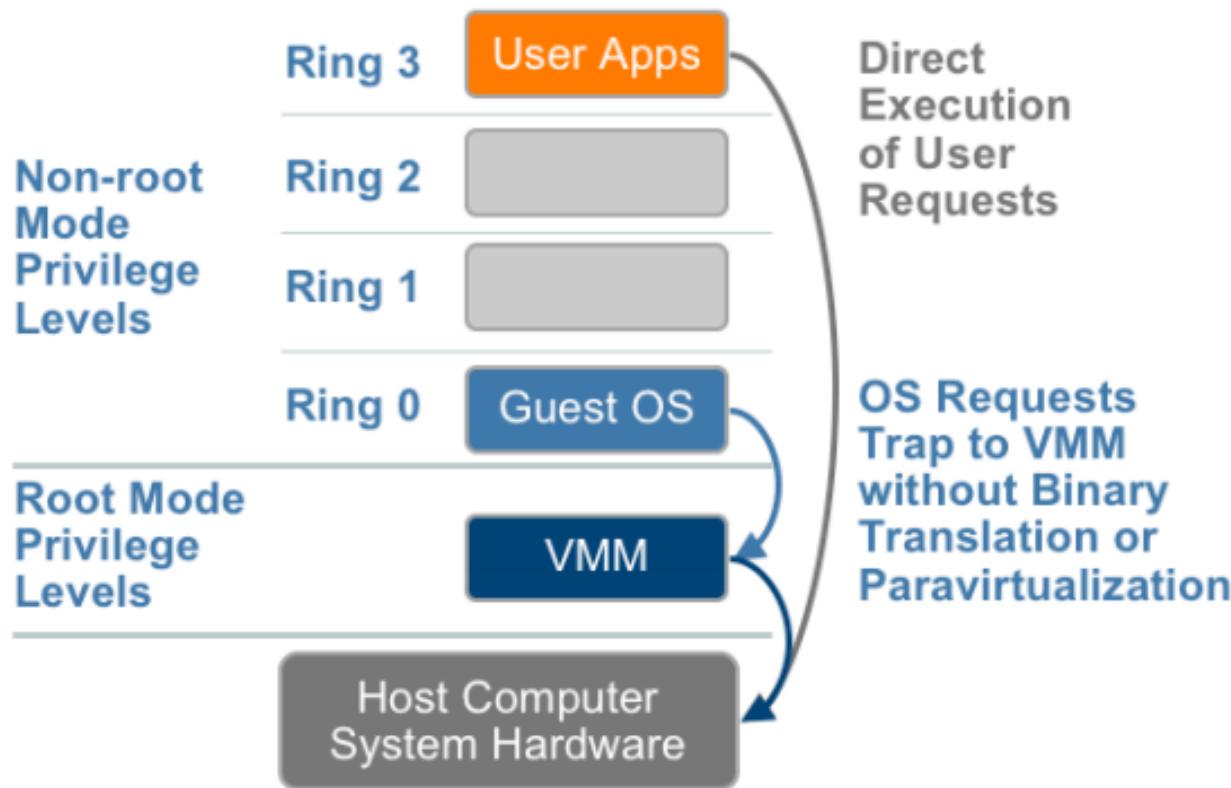
Virtualization Techniques

- **OS Assisted Virtualization or Paravirtualization**



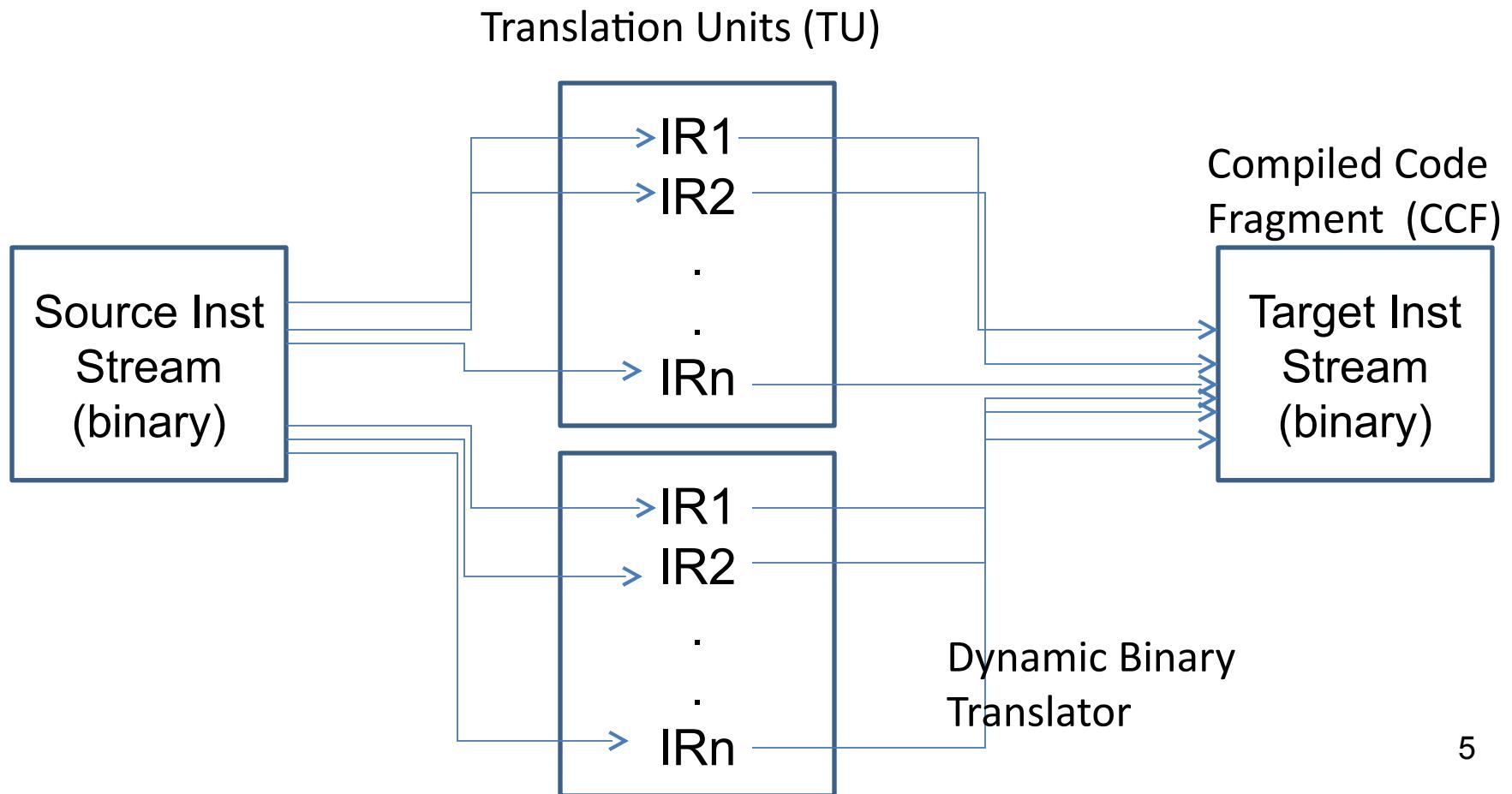
Virtualization Techniques

➤ Hardware Assisted Virtualization



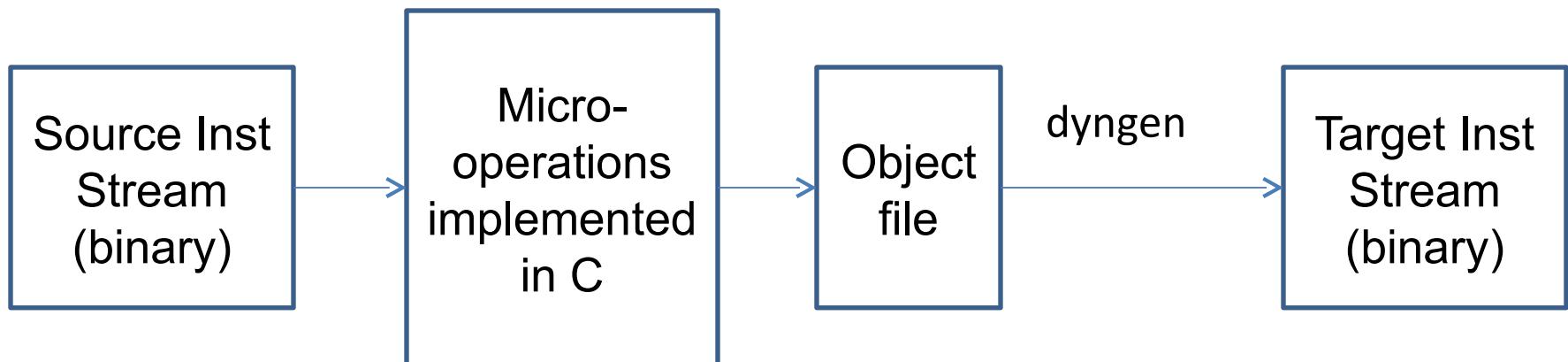
Binary Translation

- **VMWare Software Virtualization**



Binary Translation

- **QEMU Binary Translation in brief**



Quick EMUlation (QEMU)

- Machine Emulator
- Virtualizer

QEMU modes:

- User-mode emulation – Allows a process built for one CPU to be executed on another.
- System-mode emulation – Allows emulation of a full system, including processor and assorted peripherals.

References

- A comparison of software and hardware techniques for x86 virtualization – Keith Adams, Ole Agesen, ASPLOS'06
- Understanding Full Virtualization, Paravirtualization and Hardware Assist – VMware Whitepaper
- QEMU, a fast and portable Dynamic Translator – Fabrice Bellard
- QEMU Wiki: wiki.qemu.org

QEMU Deep Dive

Source: wiki.qemu.org

Different ISAs

Register	Value
r0	1
r1	2
...	...

code:

```
bb 01 00 00 00 mov $0x1,%ebx  
89 d8          mov %ebx,%eax  
83 c3 01       add $0x1,%ebx
```

code:

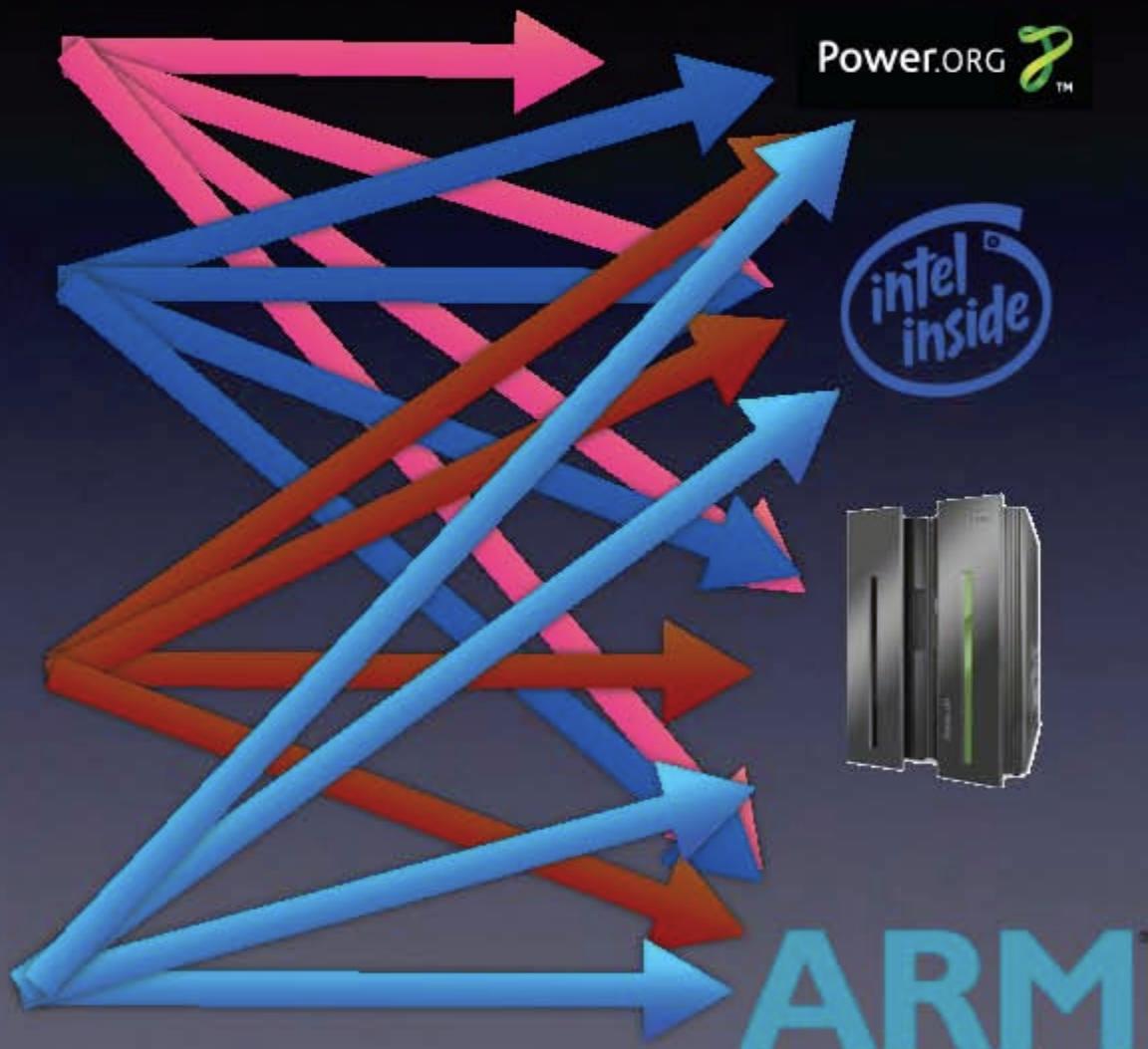
```
38 20 00 01 li      r1,1  
7C 20 0b 78 mr      r0,r1  
38 21 00 01 addi    r1,r1,1
```

Register	Value
eax	0
ebx	0
...	...

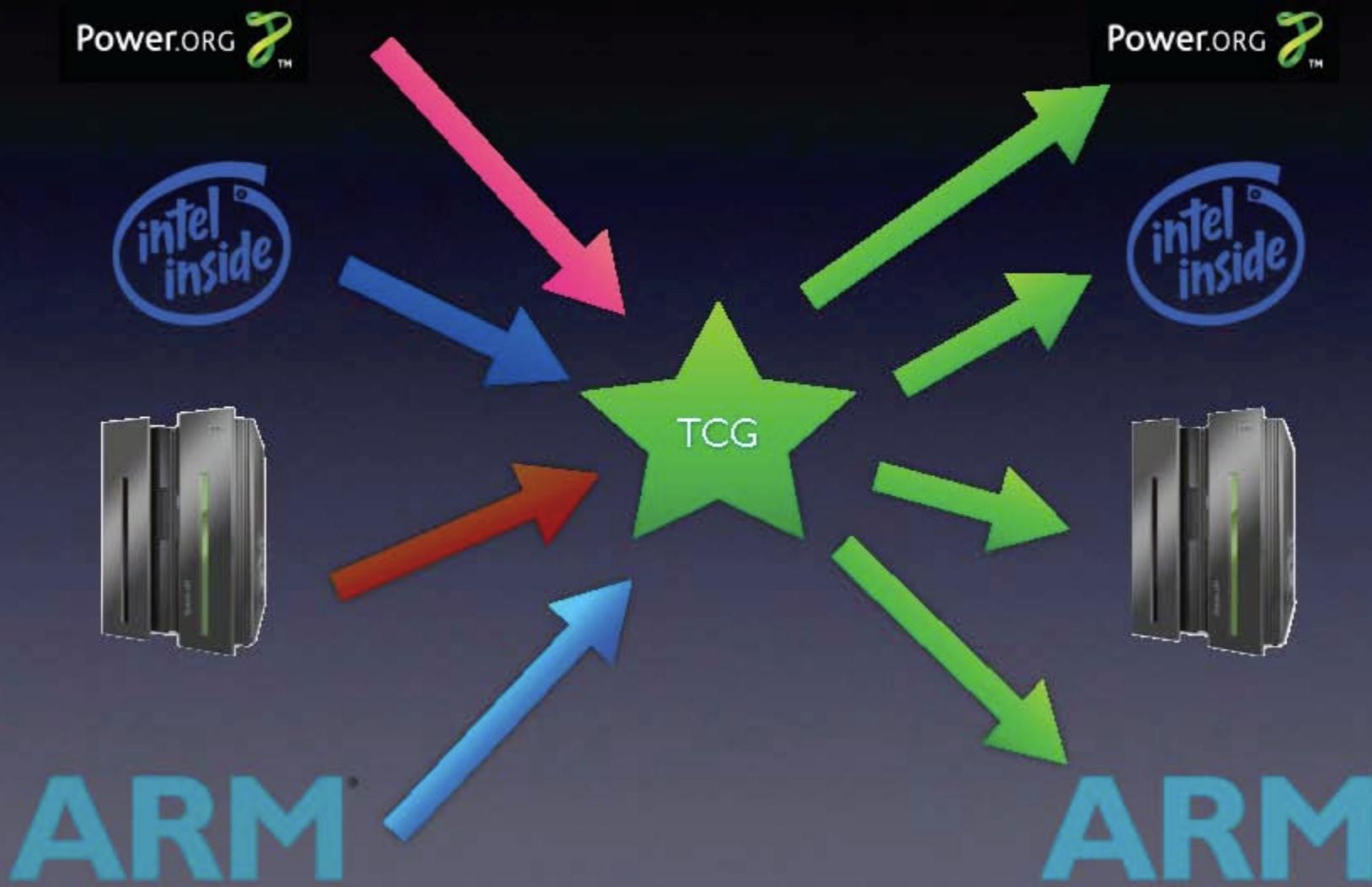
Converting code

Power.ORG 

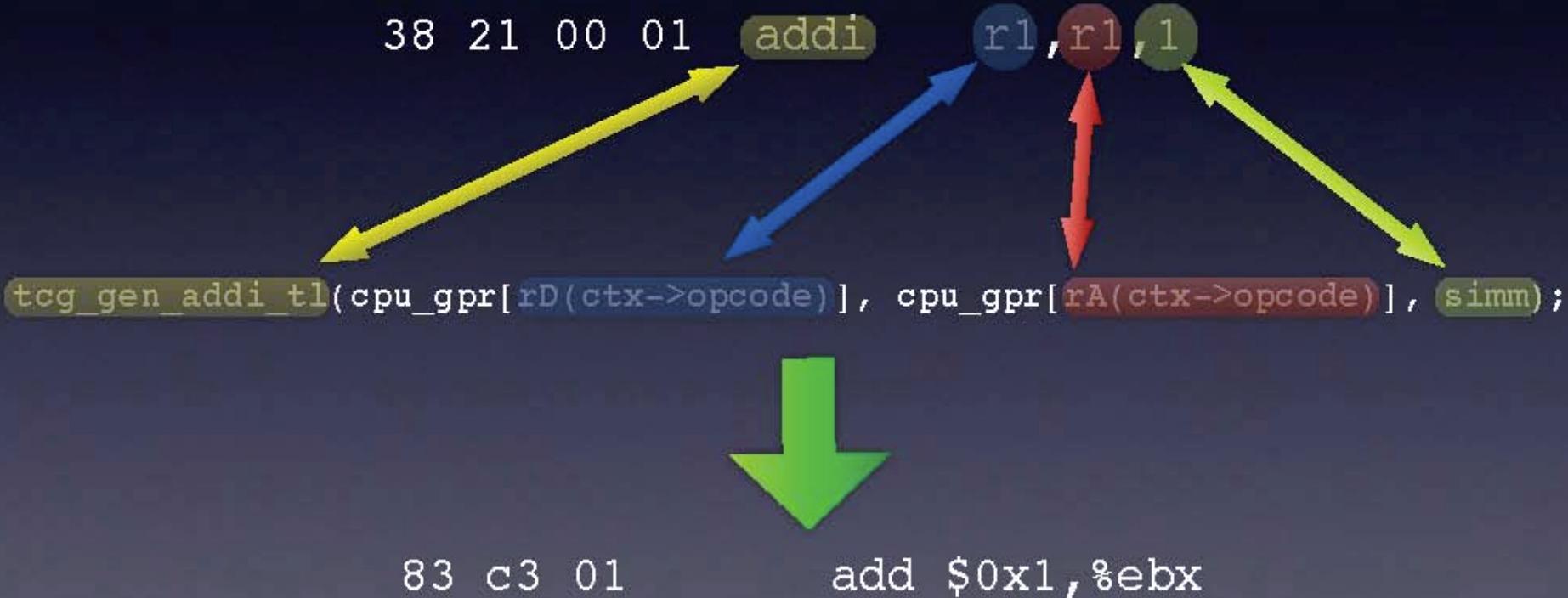
Power.ORG 



TCG

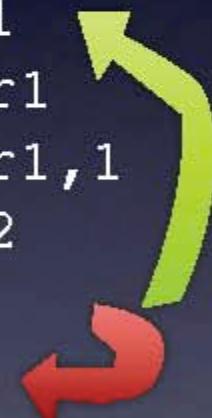


TCG micro-ops



Translation Blocks

0:	38	20	00	01	li	r1,1
4:	7c	20	0b	78	mr	r0,r1
8:	38	21	00	01	addi	r1,r1,1
c:	2c	01	00	02	cmpwi	r1,2
10:	41	82	ff	f0	beq+	0x0



Translation Blocks

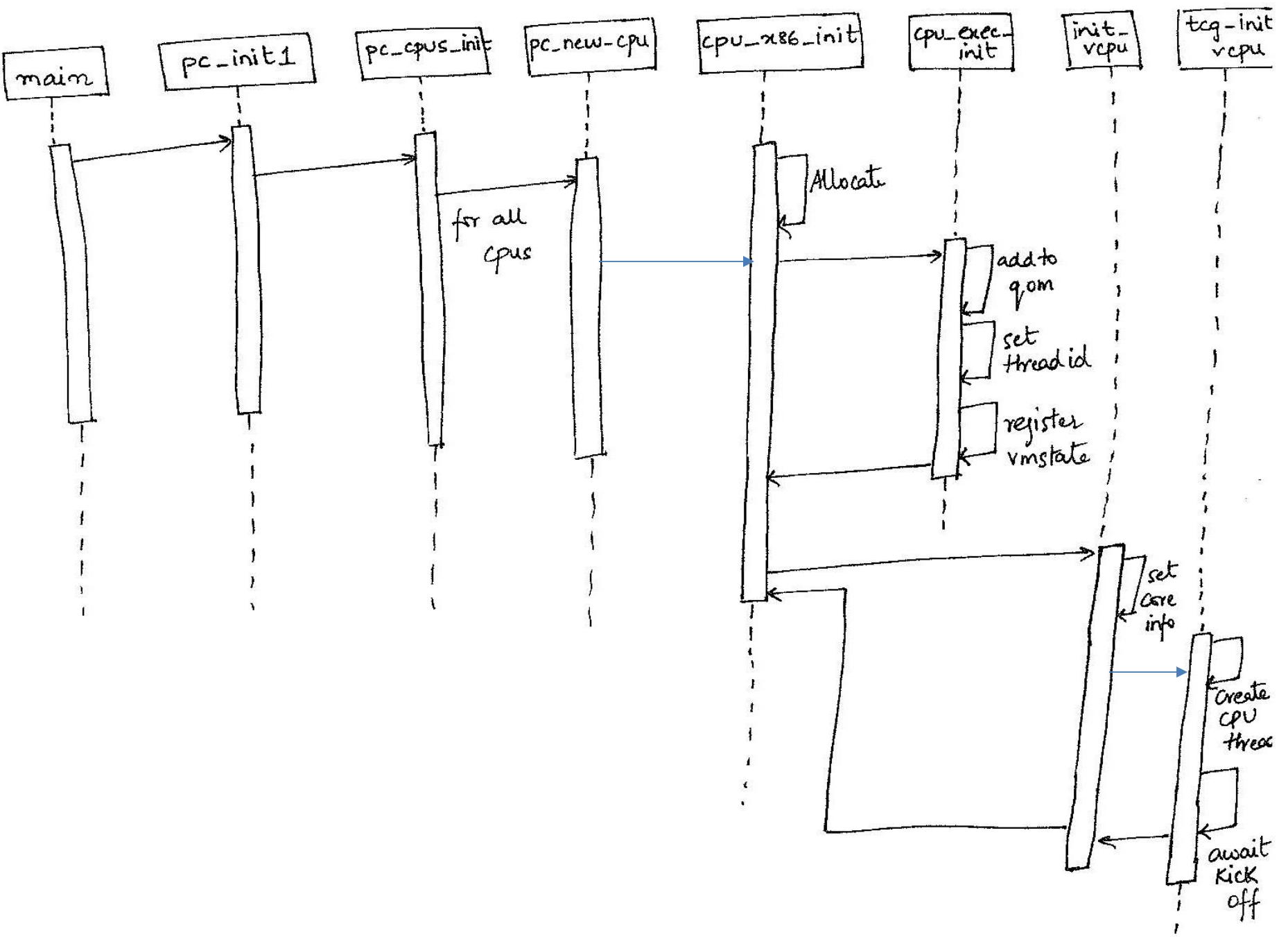


TB Chaining

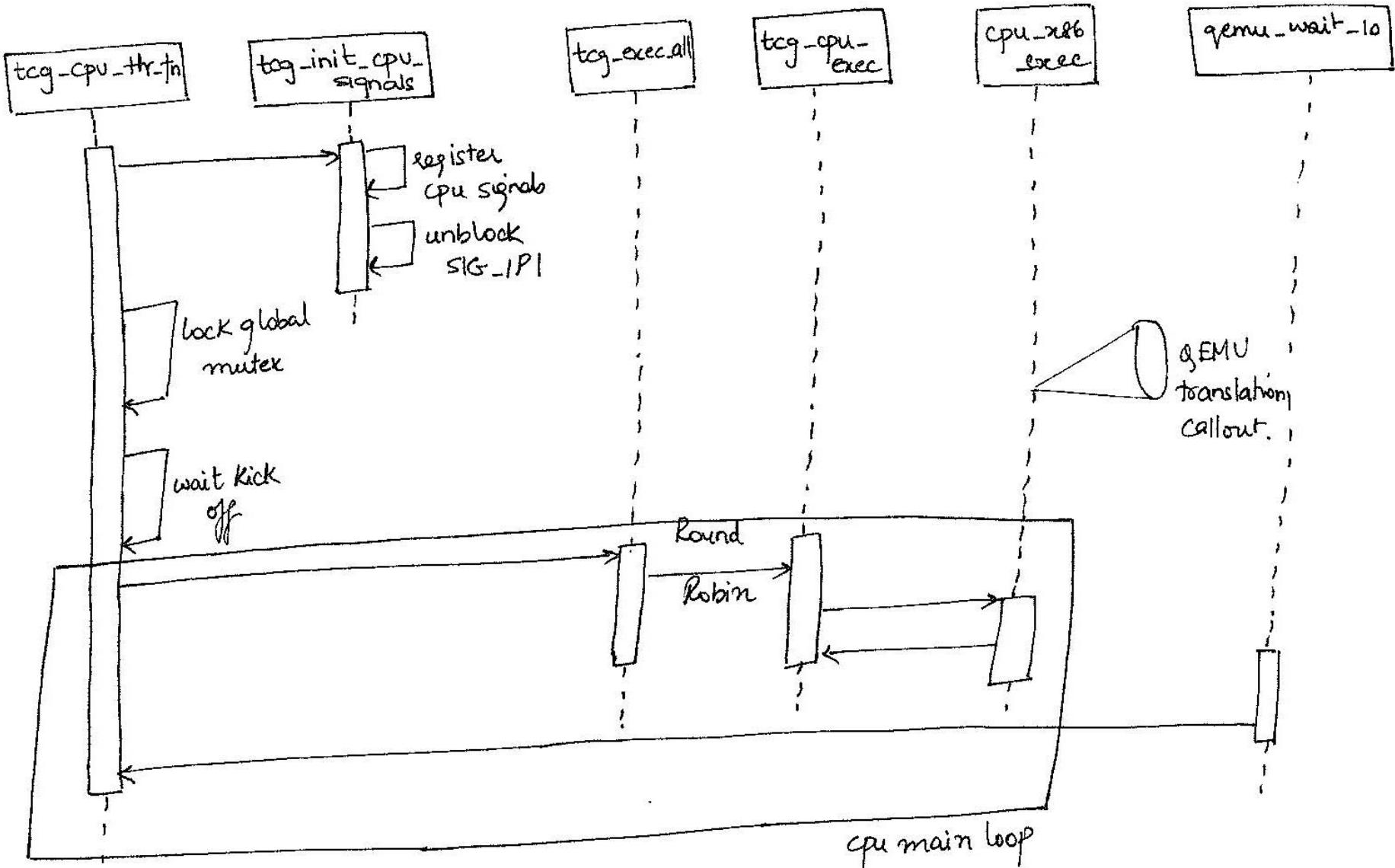


QEMU ARCHITECTURE

GEMU CPU INITIALIZATION



CPU THREAD EMULATION



Thank you