

# ATWILC1000/ATWILC3000

# Wi-Fi® Link Controller Linux® User Guide

# Introduction

This user guide describes how to run Wi-Fi on the ATWILC1000 SD card or the ATWILC3000 Shield board on the SAMA5D4 Xplained Ultra running with the Linux<sup>®</sup> kernel 4.9.

Note: All references to the ATWILC module includes all the devices listed below unless otherwise noted:

- ATWILC1000
- ATWILC3000

The source codes are maintained on GitHub. For latest source codes, see GitHub Linux for ATWILC at https://github.com/linux4wilc.

### Figure 1. ATWILC1000 SD Card and ATWILC3000 Shield Board







# Table of Contents

Intr	troduction	1		
1.	Prerequisites	4		
2.	<ul> <li>Building Linux for SAMA5D4 Xplained Ultra Board.</li> <li>2.1. Cloning a Kernel Source and Root File System.</li> <li>2.2. Loading SAMA5D4 Configuration File.</li> <li>2.3. Buildroot File System and Linux Kernel.</li> <li>2.4. Building Linux Kernel Individually.</li> </ul>	5 5		
3.	Updating Binary and System Image into the Target Board	7		
4.	Updating ATWILC Firmware 4.1. ATWILC1000 and ATWILC3000 Driver Modules 4.2. ATWILC1000 and ATWILC3000 Firmware Binaries	9		
5.	Running ATWILC	10 11 12 14 16 18 18 20 22 22 23 25 25 25		
6.	Document Revision History			
The	ne Microchip Web Site			
Cu	ustomer Change Notification Service	34		
Cu	Customer Support			
Mic	icrochip Devices Code Protection Feature			
Leç	Legal Notice			
Tra	ademarks	35		
Qu	uality Management System Certified by DNV			

# 1. **Prerequisites**

The build prerequisite for Linux is a host PC with Linux operating system. The hardware prerequisites are the following:

- Linux
  - SAMA5D4 Xplained Ultra
  - ATWILC1000 SD Pro card
  - ATWILC3000 Shield board
  - USB to Serial adapter (for DEBUG port)
- Common
  - Micro-USB cable (Micro-A/Micro-B)

To avoid modifying kernel code, mount the resistor R312 with an approximate value of 120k Ohm in the location shown below on the ATWILC3000 Shield board.



# 2. Building Linux for SAMA5D4 Xplained Ultra Board

This section describes how to build the root file system and kernel image to use for ATWILC devices demo.

This user guide describes general information on the AT91Bootstrap and U-Boot information. For more details on the AT91Bootstrap and U-Boot, see U-Boot of Linux & Open Source related information for AT91 Smart ARM Microcontrollers.

# 2.1 Cloning a Kernel Source and Root File System

The demo uses buildroot to get the suitable toolchain, root file system, and Linux kernel.

The buildroot is cloned from linux4wilc github at the following address:

\$ git clone https://github.com/linux4wilc/buildroot4wilc.git

The buildroot is cloned at the following path in the current directory:

\buildroot4wilc

The current buildroot4wilc is copied from buildroot's repository at git://git.buildroot.net/ buildroot, branch 2017\_08, modified with WILC config files (configs/sama5\_wilc\_defconfig), and other config files that help run WILC examples.

## 2.2 Loading SAMA5D4 Configuration File

Use the predefined defconfig file to create the required .config configuration file. This defconfig file is available in configs folder of the buildroot folder buildroot4wilc.

For SAMA5D4, the sama5 wild defconfig defconfig file is used.

To build the root file system for SAMA5D4 with Linux kernel 4.9 for the ATWILC board, browse to the directory where the files are extracted and create the **.config** file, using the following commands:

```
$ cd buildroot4wilc
$ make sama5_wilc_defconfig
```

# 2.3 Buildroot File System and Linux Kernel

Start the build operation using \$ make command from the buildroot directory.

This \$ make command displays the build status on the terminal.

**Note:** Ensure that the host PC is connected to the internet before starting the build operation and do not use any build options.

The rootfs.ubi file is generated in the buildroot/output/images directory when the build operation is complete. The default build will include the WILC modules in the rootfs.ubi.

The driver source files are located at: https://github.com/linux4wilc/linux-at91/tree/ master/drivers/staging/wilc1000 in the linux-at91 kernel.

**Note:** The driver directory name is wilc1000 for legacy reasons only. The driver supports both ATWILC1000 and ATWILC3000.

# 2.4 Building Linux Kernel Individually

Buildroot downloads the Linux kernel as per the buildroot configuration file from GitHub. The downloaded kernel must be available in the buildroot4wilc/output/build/linux-xxxx path, and is built automatically during the buildroot build operation.

However, if the kernel is modified after building the buildroot, the user must rebuild the kernel. The following is the procedure to build the Linux kernel against the toolchain and ARM architecture:

1. Change the directory to the Linux kernel source folder, using the following command:

\$ cd output/build/linux-xx

2. Create the kernel with the help of sama5\_defconfig defconfig file, using the following command:

\$ make ARCH=arm sama5\_defconfig

3. Perform the required changes using the menuconfig tool, using the following command:

\$ make ARCH=arm menuconfig

4. Build the Linux kernel against the toolchain and ARM architecture, using the following commands:

```
$ make ARCH=arm CROSS_COMPILE=../../../output/host/opt/ext-toolchain/bin/arm-linux-
gnueabihf-
$ make ARCH=arm CROSS_COMPILE=../../../output/host/opt/ext-toolchain/bin/arm-linux-
gnueabihf- zImage
$ make ARCH=arm CROSS_COMPILE=../../output/host/opt/ext-toolchain/bin/arm-linux-
gnueabihf- dtbs
```

# 3. Updating Binary and System Image into the Target Board

This section describes how to update or flash the system image. The pre-build images include pre-build driver and firmware binaries, which are available at GitHub.

The SAM-BA<sup>®</sup> tool is used to flash the binaries into the target board.

**Note:** Ensure that the SAM-BA tool is installed in the host machine before updating the system image. The scripts in the demo package can use either SAM-BA 2.16 or 3.2.x depending on the download script the user selects in step 5 of the following procedure.

For additional information, refer to the following:

- Software Tools
- SAMA5D4 Xplained Board
- ATSAMA5D44 Microprocessor

To start flashing, perform the following steps:

- 1. Download the pre-built images from https://github.com/linux4wilc/wilc\_demo.
- 2. Unzip the downloaded file.
- 3. Once the new image is built as described in Chapter 2, Building Linux for SAMA5D4 Xplained Ultra Board, these files must be copied from the buildroot\output\images directory to the directory where the demo\_linux\_nandflash.tcl file is available.

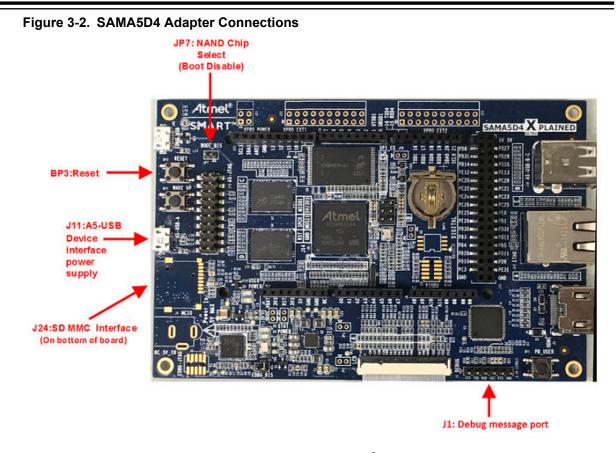
### Figure 3-1. List of Files in buildroot\output\images Location

Name	Date modified	Туре	Size
at91bootstrap-sama5d4_xplained.bin	16/4/2016 3:34 PM	BIN File	20 KB
at91-sama5d4_xplained.dtb	28/7/2016 6:53 PM	DTB File	31 KB
atmel-xplained-demo-image-sama5d4-xplained.ubi	16/4/2016 3:35 PM	UBI File	148,736 KB
🖾 demo_linux_nandflash.bat	18/4/2016 11:18 A	Windows Batch File	1 KB
demo_linux_nandflash.sh	18/4/2016 11:18 A	SH File	1 KB
demo_linux_nandflash.tcl	18/4/2016 11:18 A	TCL File	1 KB
demo_script_linux_nandflash.tcl	18/4/2016 11:18 A	TCL File	11 KB
README	18/4/2016 11:18 A	File	2 KB
🖀 rootfs.tar	9/8/2016 5:39 PM	WinRAR archive	152,610 KB
🖀 rootfs.tar.gz	9/8/2016 5:39 PM	WinRAR archive	66,200 KB
rootfs.ubi	9/8/2016 5:39 PM	UBI File	97,024 KB
ootfs.ubifs	9/8/2016 5:39 PM	UBIFS File	93,496 KB
u-boot-sama5d4-xplained.bin	16/4/2016 3:34 PM	BIN File	332 KB
zImage	28/7/2016 6:53 PM	File	3,630 KB
zImage-sama5d4-xplained.bin	16/4/2016 3:34 PM	BIN File	3,470 KB

4. Add the jumper at JP7 and connect to the host PC via the USB port at J11. Ensure that the host machine completes the USB serial port connection and then remove the jumper at JP7. The following figure shows the SAMA5D4 adapter connections.

# ATWILC1000/ATWILC3000

## Updating Binary and System Image into the ...



5. Execute the demo\_linux\_nandflash.bat (for Windows<sup>®</sup>) file or the demo\_linux\_nandflash.sh (for Linux) file.

Note:

- By default, the demo\_linux\_nandflash.sh file has sam-ba binary for 32-bit operating system. For 64-bit operating system, change the sam-ba to sam-ba\_64 in the same file.
- Execute the script in the super user mode. If sam-ba 3.2 is installed, use demo\_linux\_nandflash\_3\_2.bat or demo\_linux\_nandflash\_3\_2.sh instead.

The output log can be viewed via J1 serial port. Open the serial terminal on PC via the COM port, with the following configurations:

- 115200 baud rate
- 8-bit data
- No parity
- One stop bit
- No flow control
- 6. Successful download of the system image into the board is indicated by a log file, which opens automatically. This log file contains all the download process history.

## Updating ATWILC Firmware

# 4. Updating ATWILC Firmware

This chapter describes how to update the ATWILC firmware or driver on the demo image.

## 4.1 ATWILC1000 and ATWILC3000 Driver Modules

After the system boots, add the ATWILC driver modules wilc-sdio.ko, or wilc-spi.ko to /lib/ modules/4.9.xx-XX/kernel/drivers/staging/wilc1000/ directory or copy to any location on the file system.

## 4.2 ATWILC1000 and ATWILC3000 Firmware Binaries

- Add the ATWILC1000 firmware wilc1000\_wifi\_firmware.bin to the /lib/firmware/ mchp/ directory.
- Add the ATWILC3000 Wi-Fi firmware, wilc3000\_wifi\_firmware.bin to the/lib/firmware/ mchp/ directory.
- 3. Add the ATWILC3000 Bluetooth<sup>®</sup> firmware, wilc3000\_ble\_firmware.bin to the wilc/lib/ firmware/mchp/ directory.

Note: The firmware is available at https://github.com/linux4wilc/firmware.

The files can be transferred into the SAMA5D4 platform using any of the following methods:

- Ethernet
- ZMODEM

### 4.2.1 Adding Files Using Ethernet

The Local Area Network (LAN)/ Wide Area Network (WAN) can be used to transfer the file from one machine to another machine, using the following command:

\$ scp [path of file to send] root@[receiver's IP]:[target directory]

For example, the following command sends the wilcl000\_wifi\_firmware.bin file from the binary directory to the /lib/firmware/mchp directory of the device using the internal IP address 192.168.0.11.

\$ scp binary/wilc1000\_wifi\_firmware.bin root@192.168.0.11: /lib/firmware/mchp

### 4.2.2 Adding Files Using ZMODEM

The ZMODEM file transfer protocol also can be used to transfer the files.

In Teraterm, change the target location directory using the following command:

\$ cd Target\_location

Execute the ZMODEM command using the following command:

\$ rz

In Teraterm, from the File menu, choose Transfer > Send, then browse and select the desired file.

# 5. Running ATWILC

This chapter describes how to use the ATWILC1000 and ATWILC3000 on the SAMA5D4 Xplained Board or any similar Linux platform.

## 5.1 Accessing the Console

The user can access the serial console through the on board serial-to-USB converter. In fact, the Embedded Debugger (EDBG) chip on the evaluation kit acts as a serial-to-USB converter and is loaded with a firmware that can communicate via USB-CDC protocol.

To enable EDBG, open JP1 and connect the USB cable to the board (J20 EDBG-USB).

### 5.1.1 For Microsoft Windows Users

Install USB drivers for Atmel and Segger tools. Then, identify the USB connection that is established. The user can verify this by checking if the EDBG virtual COM port appears in the Device Manager. The COMxx number is used to configure the terminal emulator.

### 5.1.2 For Linux Users

Identify the USB connection by monitoring the last lines of dmesg command. The /dev/ttyACMx number is used to configure the terminal emulator.

The following is the USB debug port connection:

[172677.700868] usb 2-1.4.4: new full-speed USB device number 31 using ehci-pci [172677.792677] usb 2-1.4.4: not running at top speed; connect to a high speed hub [172677.793418] usb 2-1.4.4: New USB device found, idVendor=03eb, idProduct=6124 [172677.793424] usb 2-1.4.4: New USB device strings: Mfr=0, Product=0, SerialNumber=0 [172677.793897] cdc\_acm 2-1.4.4:1.0: This device cannot do calls on its own. It is not a modem. [172677.793924] cdc acm 2-1.4.4:1.0: ttyACM0: USB ACM device

The identifiers **idVendor=03eb**, and **idProduct=6124** indicate the device as the evaluation kit board with USB connection.

Now, use the terminal emulator with appropriate terminal settings (see Table 5-1) to communicate with the SAMA5D4 adapter.

### 5.1.3 Serial Communication Parameters

The serial communication parameters are as follows:

### Table 5-1. Serial Port Settings

Function	Settings
Baud rate	115200
Data	8-bit
Parity	None
Stop	1-bit
Flow control	None

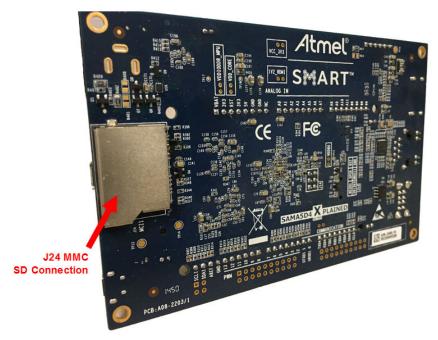
### 5.2 Recognizing ATWILC1000

The following section describes the SD express board and Serial Peripheral Interface (SPI) board connections.

### 5.2.1 SD Express Board

Before performing the boot-up operation, ensure that the ATWILC1000 SD Express board is connected in the SD slot (J24) of the SAMA5D4 Xplained board (see following figure).

### Figure 5-1. SAMA5D4 SD Connection



The Secure Digital Input/Output (SDIO) Express card is recognized during boot-up with the following lines.

mmc0: new high speed SDIO card at address 0001

Use the following commands to load the ATWILC1000 module SDIO driver.

```
Welcome to Buildroot
buildroot login: root
[root@buildroot ~]# insmod wilc.ko
wilc: module is from the staging directory, the quality is unknown, you have been warned.
[root@buildroot ~]# insmod wilc-sdio.ko
wilc_sdio: module is from the staging directory, the quality is unknown, you have been
warned.
linux_sdio_probe init_power =0
wilc_sdio mmc0:0001:1:Driver Initializing success
```

Note: Do not panic upon receiving the following message while loading the module:

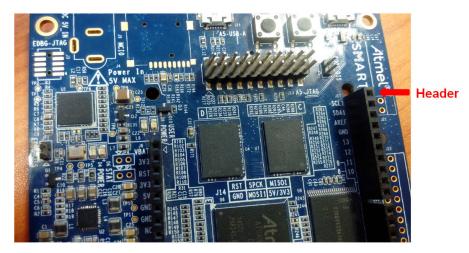
wilc: module is from the staging directory, the quality is unknown, you have been warned

This is the default message for all the drivers in kernel staging directory.

### 5.2.2 Serial Peripheral Interface Board

The ATWILC1000 Serial Peripheral Interface (SPI) board must be connected to SPI1 interface at J17 as shown in the following figure.

#### Figure 5-2. SAMA5D4 SPI Connection



### Table 5-2. SPI Pin Descriptions

SPI Pins	Header J17 Pins
MOSI	PIN11
CLK	PIN13
MISO	PIN12
CS	PIN10
IRQ	PIN8

**Note:** VEXT pin in the SPI card can be connected to 3V3 pin in the header J6. Re-configure to build the driver in SPI mode with the WILC\_SPI option in the kernel menuconfig. The modules wilc.ko and wilc-spi.ko need to be loaded for the ATWILC1000 SPI driver.

## 5.3 Recognizing ATWILC3000

The following section describes the SDIO shield board and SPI shield board connections.

### 5.3.1 SDIO Shield Board

Before performing the boot-up operation, ensure that the ATWILC3000 Shield board is connected to the Shield Arduino Shield Stacking Connector of the SAMA5D4 Xplained adapter.

Load the Wi-Fi SDIO driver module using the following command:

```
[root@buildroot ~]# insmod wilc.ko
wilc: module is from the staging directory, the quality is unknown, you have been warned.
[root@buildroot ~]# insmod wilc-sdio.ko
wilc_sdio: module is from the staging directory, the quality is unknown, you have been warned.
linux_sdio_probe init_power =0
wilc_sdio mmc0:0001:1: Driver Initializing success
```

Note: Do not panic upon receiving the following message while loading the module:

wilc: module is from the staging directory, the quality is unknown, you have been warned

This is the default message for all the drivers in kernel staging directory.

### 5.3.2 Serial Peripheral Interface Shield Board

The ATWILC3000 Shield boards can operate using both SDIO and SPI, and are configured by installing or removing 0 Ohm resistors. By default, the boards are preconfigured for SDIO mode.

To switch to the SPI mode, the user must change the following resistors as shown in the following illustration.

### Figure 5-3. ATWILC3000 Shield Board Configured for SPI



The resistors marked in green arrows must be connected and those marked in red arrows must be removed.

 Table 5-3.
 SPI Resistor Configuration

Resistors to be Removed	Resistors to be Connected
R311	R310
R218	R214
R219	R215
R220	R216
R221	R217

#### 1. Load the Wi-Fi SDIO driver module, using the following command:

```
# modprobe wilc-spi
wilc_spi: module is from the staging directory, the quality is unknown, you have been
warned.
WILC_SPI spi32765.0: spiModalias: wilc spi, spiMax-Speed: 48000000
```

# ATWILC1000/ATWILC3000

## Running ATWILC

```
(unnamed net_device) (uninitialized): INFO [wilc_create_wiphy]Registering wifi device
(unnamed net_device) (uninitialized): INFO [WILC_WFI CfgAlloc]Allocating wireless device
(unnamed net device) (uninitialized): INFO [wilc_create_wiphy]Successful Registering
(unnamed net_device) (uninitialized): INFO [wilc_create_wiphy]Registering wifi device
(unnamed net_device) (uninitialized): INFO [WILC_WFI_CfgAlloc]Allocating wireless device
(unnamed net device) (uninitialized): INFO [wilc create wiphy]Successful Registering
WILC SPI spi32765.0: WILC got 60 for gpio reset
WILC_SPI spi32765.0: WILC got 94 for gpio_chi
wifi_pm : 0
WILC_SPI spi32765.0: WILC got 94 for gpio_chip_en
wifi pm : 1
WILC SPI spi32765.0: WILC SPI probe success
# ifconfig wlan0 up
WILC_SPI spi32765.0 wlan0: INFO [wilc_mac_open]MAC OPEN[d477d800] wlan0
WILC POWER UP
WILC SPI spi32765.0 wlan0: INFO [wilc init host int]Host[d477d800][d477cc00]
WILC_SPI spi32765.0 wlan0: INFO [wilc_mac_open]*** re-init ***
WILC_SPI spi32765.0 wlan0: INFO [wlan_init_locks]Initializing Locks ...
WILC_SPI spi32765.0 wlan0: INFO [wilc_wlan_init]Initializing WILC_Wlan ...
WILC_SPI spi32765.0 wlan0: INFO [init_chip]Bootrom sts = c
WILC SPI spi32765.0 wlan0: INFO [wilc wlan initialize]WILC Initialization done
WILC_SPI spi32765.0 wlan0: INFO [init_irq]TRQ request succeeded IRQ-NUM= 137 on GPIO: 91
WILC_SPI spi32765.0 wlan0: INFO [wlan_initialize_threads]Initializing Threads ...
WILC SPI spi32765.0 wlan0: INFO [wlan initialize threads]Creating kthread for
transmission
WILC SPI spi32765.0 wlan0: INFO [wlan initialize threads]Creating kthread for Debugging
WILC_SPI spi32765.0 wlan0: INFO [wilc_wlan_get_firmware]Detect chip WILC3000
WILC_SPI spi32765.0 wlan0: INFO [wilc_wlan_get_firmware]loading firmware mchp/
wilc3000 wifi firmware.bin
WILC SPI spi32765.0 wlan0: INFO [wilc wlan get firmware]WLAN firmware: mchp/
wilc\overline{3}000 wifi firmware.bin
WILC_SPI spi32765.0 wlan0: INFO [wilc_firmware_download]Downloading Firmware ...
WILC_SPI spi32765.0 wlan0: INFO [wilc_wlan_firmware_download]Downloading firmware size =
1371\overline{7}2
WILC SPI spi32765.0 wlan0: INFO [wilc wlan firmware download]Offset = 120228
WILC SPI spi32765.0 wlan0: INFO [wilc wlan firmware download]Offset = 137172
WILC_SPI spi32765.0 wlan0: INFO [wilc_firmware_download]Download Succeeded
WILC_SPI spi32765.0 wlan0: INFO [linux_wlan_start_firmware]Starting Firmware ...
WILC SPI spi32765.0 wlan0: INFO [linux wlan start firmware]Waiting for Firmware to get
ready
WILC SPI spi32765.0 wlan0: INFO [linux wlan start firmware]Firmware successfully started
WILC_SPI spi32765.0 wlan0: INFO [wilc_wlan_initialize]WILC Firmware Ver
WILC WIFI FW REL 15 00 RC4 Build: 9153
[root@buildroot ~]#
```

### 5.4 Modifying Configuration Files

To use the Wi-Fi module, the user must load a set of default configuration files on the prebuilt image. These files can be modified as per the requirement described in the following section.

### 5.4.1 Wi-Fi Protected Access Supplicant

The reference configuration files for Wi-Fi Protected Access (WPA) supplicant are available in: /etc/ directory. The configuration files for both Station and Access Point modes are available in the demo prebuilt image.

#### 5.4.1.1 Station Mode

The configuration file for Station mode wilc\_wpa\_supplicant.conf contains the following lines.

```
ctrl_interface=/var/run/wpa_supplicant
update_config=1
```

# ATWILC1000/ATWILC3000

### Running ATWILC

#### 5.4.1.2 Access Point Open Security Mode

The Access Point (AP) mode configuration file with open security wilc\_hostapd\_open.conf contains the following lines.

interface=wlan0 driver=nl80211 ctrl\_interface=/var/run/hostapd ssid=wilc1000\_SoftAP dtim\_period=2 beacon\_int=100 channel=7 hw\_mode=g max\_num\_sta=8 ap max\_inactivity=300

#### 5.4.1.3 Access Point Wired Equivalent Privacy Security Mode

The AP mode configuration file for Wired Equivalent Privacy (WEP) Security wilc\_hostapd\_wep.conf contains the following lines.

```
interface=wlan0
driver=n180211
ctrl_interface=/var/run/hostapd
ssid=wilc1000_SoftAP
dtim_period=2
beacon_int=100
channel=7
hw_mcde=g
max_num_sta=8
ap_max_inactivity=300
ieee80211n=1
auth_algs=1
```

#### 5.4.1.4 WPA Security Mode

The AP mode configuration file with WPA security wilc\_hostapd\_wpa.conf contains the following lines.

wpa\_passphrase=12345678 wpa\_key\_mgmt=WPA-PSK wpa\_pairwise=TKIP CCMP rsn\_pairwise=CCMP

#### 5.4.2 Dynamic Host Configuration Protocol

The reference configuration file for the Dynamic Host Configuration Protocol (DHCP) server is available in the /etc/dhcp/dhcpd.conf file.

```
ddns-update-style none;
default-lease-time 600;
max-lease-time 7200;
option subnet-mask 255.255.255.0;
option domain-name-servers 168.126.63.1, 164.124.101.2; # DNS Server IP
option domain-name "sample.example"; # domain name
subnet 192.168.0.0 netmask 255.255.255.0 {
    range 192.168.0.10 192.168.0.110; # range ip
    option broadcast-address 192.168.0.255;
    option routers 192.168.0.1; # gateway ip
}
Log-facility local7;
```

Note: Each value must be modified as per the test environment.

The location of the dhcpd.conf file should match the location defined in /etc/init.d/S80dhcpserver under: test -f /etc/dhcp/dhcpd.conf || exit 0.

#### 5.4.3 radvd

For IPv6, the radvd configuration file is required. The reference file on the demo image is available in the /etc/radvd.conf directory.

```
interface wlan0
{
    AdvSendAdvert on;
    prefix 2001:db8:0:2::/64
    {
    };
};
```

## 5.5 Running in the ATWILC Station Mode

The following example shows how to run the ATWILC device in Station mode, and connect to an AP.

1. Initialize the ATWILC1000 and ATWILC3000 driver module, using the following command:

```
Welcome to Buildroot
buildroot login: root
root@buildroot ~]# modprobe wilc-sdio
wilc_sdio: module is from the staging directory, the quality is unknown, you have been
warned.
linux_sdio_probe init_power =0
wilc sdio mmc0:0001:1: Driver Initializing success
```

2. Start the WPA supplicant service and execute wpa supplicant, using the following command:

```
# wpa_supplicant -iwlan0 -Dnl80211 -c /etc/wilc_wpa_supplicant
[1] 819
[root@buildroot ~]# Successfully initialized wpa_supplicant
rfkill: Cannot open RFKILL cwnirllcd1v0c00_sdio mmc0:0001:1: chipid (001003a0)
wilc_sdio mmc0:0001:1: has_thrpt_enh3 = 1...
wilc_sdio mmc0:0001:1 wlan0: Detect chip wilc1000
wilc_sdio mmc0:0001:1 wlan0: loading firmware wilc_wifi_firmware.bin
wilc_gnrl_async_info_received
wilc_sdio mmc0:0001:1 wlan0: WILC Firmware Ver = WILC_WIFI_FW_REL_15_00 Build: 8719
```

- 3. Connect to the Access Point:
  - 3.1. To connect to an unsecured AP:

Use the following commands to scan and connect to the AP.

# wpa\_cli -p/var/run/wpa\_supplicant ap\_scan 1
# wpa\_cli -p/var/run/wpa\_supplicant add\_network
# wpa\_cli -p/var/run/wpa\_supplicant set\_network 0 ssid '"User\_AP"'
# wpa\_cli -p/var/run/wpa\_supplicant set\_network 0 key\_mgmt NONE
# wpa\_cli -p/var/run/wpa\_supplicant select network 0

Note: Change the User\_AP with the Service Set Identifier (SSID) of the desired AP.

3.2. To connect to the WPA secured Access Point: Use the following commands to scan and connect to a WPA or WPA2 and Temporal Key Integrity Protocol (TKIP) or Advanced Encryption Standard (AES) protected AP.

```
# wpa_cli -p/var/run/wpa_supplicant ap_scan 1
# wpa_cli -p/var/run/wpa_supplicant add_network
# wpa_cli -p/var/run/wpa_supplicant set_network 0 ssid '"User_AP"'
# wpa_cli -p/var/run/wpa_supplicant set_network 0 key_mgmt WPA-PSK
# wpa_cli -p/var/run/wpa_supplicant set_network 0 psk '"12345678"'
# wpa_cli -p/var/run/wpa_supplicant select_network 0
```

Note: Change the User\_AP and 12345678 with the SSID and password of desired AP.

3.3. To connect to the WEP secured Access Point:

Use the following commands to scan and connect to a WEP shared key protected AP.

```
#wpa_cli -p/var/run/wpa_supplicant ap_scan 1
#wpa_cli -p/var/run/wpa_supplicant add_network
#wpa_cli -p/var/run/wpa_supplicant set_network 0 ssid ``User_AP'''
#wpa_cli -p/var/run/wpa_supplicant set_network 0 key_mgmt NONE
#wpa_cli -p/var/run/wpa_supplicant set_network 0 wep_tx_keyidx 0
#wpa_cli -p/var/run/wpa_supplicant set_network 0 auth_alg SHARED
#wpa_cli -p/var/run/wpa_supplicant select_network 0
```

**Note:** Change the **User\_AP** and **12345** with the Service Set Identifier (SSID) and ASCII (or Hex) of desired AP.

3.4. Connect to the WPS secured Access Point Trigger WPS Push-Button mode, using the following command:

wpa\_cli wps\_pbc

(or) to connect using PIN method, use the following command:

sudo wpa\_cli wps\_pin any <the pin>

#### 4. Run the DHCP service.

If the IP address can be allocated from the AP automatically, start the DHCP client, using the following command:

#dhcpcd wlan0 &

**Note:** If the AP does not support the DHCP service, manually set the static IP address value using the ifconfig wlan0 xxx, xxx.xxx command.

5. Check and validate the connection status, using the following commands:

```
# wpa_cli status
bssid=88:9b:39:f3:d0:4d
ssid=User_AP
id=0
mode=station
pairwise_cipher=NONE
group_cipher=NONE
key_mgmt=NONE
```

```
wpa_state=COMPLETED
ip_address=192.168.43.2
address=00:80:c2:b3:d7:4d
```

The user can save and use the network information to automatically connect to the network using the wpa\_cli save command in Linux.

## 5.6 Running in the ATWILC AP Mode

This section describes how to connect a device to the ATWILC1000 Access Point.

1. Initialize the ATWILC1000 or ATWILC3000 driver module, using the following command:

```
[root@buildroot ~]# modprobe wilc-sdio
wilc_sdio: module is from the staging directory, the quality is unknown, you have been
warned.
linux_sdio_probe init_power =0
wilc sdio mmc0:0001:1: Driver Initializing success
```

2. Run hostapd as user configuration, using the following command:

```
[root@buildroot ~]# hostapd /etc/wilc_hostapd_open.conf -B &
[root@buildroot ~]# Configuration file: /etc/wilc_hostapd_open.conf
rfkill: Cannot open RFKILL control device
wilc_sdio mmc0:0001:1 wlan0: Detect chip WILC3000
wilc_sdio mmc0:0001:1 wlan0: loading firmware wilc3000_wifi_firmware.bin
wilc_gnrl_async_info_received
wilc_sdio mmc0:0001:1 wlan0: WILC Firmware Ver = WILC_WIFI_FW_REL_15_00 Build: 8719
Using interface wlan0 with hwaddr fa:f0:05:f6:56:6a and ssid "wilc_SoftAP"
wilc_gnrl_async_info_received
wilc_sdio mmc0:0001:1 wlan0: there is no current Connect Request
wlan0: interface state UNINITIALIZED->ENABLED
wlan0: AP-ENABLED
```

Note: See the wilc\_hostapd\_open.conf file for unencrypted AP settings, wilc\_hostapd\_wep.conf file for WEP AP settings and wilc\_hostapd\_wpa.conf file for WPA/WPA2 AP settings.

3. Run DHCP server to allocate IP to client. Set the IP address to the gateway using the #ifconfig wlan0 192.168.0.1 command.

**Note:** The gateway IP address is defined in the dhcpd.conf file.

Start the DHCP server using the #/etc/init.d/S80dhcp-server start command.

The user can now connect the PC or smartphone to the ATWILC1000 access point.

To configure AP in the WPS mode, use the same steps for WPA/WPA2 settings, then use the following command to configure to the Push-button mode:

hostapd\_cli wps\_pbc

(or) to configure for the pin mode, use the following command:

hostapd\_cli wps\_pin any <pin>

## 5.7 Running in the ATWILC P2P Mode

A P2P group includes two devices: One device acts as a P2P Group Owner (GO) and the other device acts as a P2P Client. The ATWILC devices support both P2P GO and P2P Client modes. The following is the procedure to test P2P mode on ATWILC.

There are two scenarios in which the P2P mode can be tested. The following section describes each scenario:

© 2018 Microchip Technology Inc.

#### Scenario 1 - WILC device as a group owner and mobile phone as a P2P client

Configuring the WILC device as a group owner:

1. Load both the WILC modules, using the following command:

```
modprobe wilc-sdio
echo <mode> > /sys/wilc/p2p mode
```

where, mode = 1 for P2P GO and mode = 0 for P2P Client.

2. Start the WPA supplicant service and open the P2P device, using the following command:

wpa\_supplicant -Dnl80211 -ip2p0 -c/etc/wilc\_p2p\_supplicant.conf &

3. Configure the IP address of the P2P GO and start the DHCP server, using the following command:

```
ifconfig p2p0 192.168.0.1
/etc/init.d/S80dhcp-server start
```

4. On the terminal, enter into wpa\_cli interactive mode, using the following command:

wpa\_cli -ip2p0

5. Scan for neighbouring P2P devices for specified duration, using the following command:

p2p\_find <scan\_duration\_in\_seconds>

6. After scan is complete, list the available P2P peers using the following command:

p2p\_peers

This command lists the BSSID of the P2P peer.

7. Connect to the P2P Client using the BSSID of the P2P peer, using the following command:

p2p\_connect <MAC\_ADDRESS> pbc

Configuring a mobile phone as a P2P client:

In the Wi-Fi settings menu on the phone, enter into Wi-Fi Direct<sup>®</sup> mode and perform the following to establish the connection.

- Trigger connection from WILC:
  - 1.1.Enter p2p\_find command without timeout value on the WILC.The SSID of the P2P peer appears on the phone.
  - 1.2. Enter the p2p\_connect command as shown above in the WILC. A pop-up window appears on the phone.
  - 1.3. Click the Accept button or prompt to connect.
  - Trigger connection from phone:
    - 2.1. Click the SSID displayed on the phone and send a P2P invite.
    - 2.2. Enter the p2p\_connect <MAC\_ADDRESS> pbc command in the WILC to form a P2P group.

#### Scenario 2 - WILC device as a P2P client and mobile phone as a group owner

Configuring WILC device as a P2P client:

1. Load both the WILC modules, using the following command:

modprobe wilc-sdio

# ATWILC1000/ATWILC3000

## Running ATWILC

- 2. Start the WPA supplicant service and open the P2P device, using the following command: wpa supplicant -Dnl80211 -ip2p0 -c/etc/wilc p2p supplicant.conf &
- 3. On the terminal, enter into wpa\_cli interactive mode, using the following command: wpa\_cli -ip2p0
- 4. Scan for neighbouring P2P devices for specified duration, using the following command: p2p\_find <scan\_duration\_in\_seconds>
- 5. After the scan is complete, list the available P2P peers, using the following command: p2p\_peers

This command lists the BSSID of the P2P peer.

- 6. Connect to the P2P Go using the BSSID of the P2P peer, using the following command: p2p connect <MAC ADDRESS> pbc go intent=1
- 7. Press Ctrl+c to exit the interactive mode.
- 8. Run the DHCP client on the WILC to obtain IP address.

dhcpcd p2p0 &

Configuring a mobile phone as a group owner:

In the Wi-Fi settings menu on the phone, enter into Wi-Fi Direct mode and perform the following to establish the connection.

- Trigger connection from WILC:
  - 1.1. Enter the p2p\_find command without time-out value on the WILC. The SSID of the P2P peer appears on the phone.
  - 1.2. Enter the p2p\_connect command as shown above in the WILC. A pop-up window appears on the phone.
  - 1.3. Click the Accept button or prompt to connect.
- Trigger connection from phone:
  - 2.1. Click the SSID displayed on the phone and send a P2P invite.
  - 2.2. Enter the p2p\_connect <MAC\_ADDRESS> pbc command in the WILC to form a P2P group.

## 5.8 Supported Modes with Concurrency

The ATWILC devices support the following modes to execute concurrently.

- STA STA (see *Running in the ATWILC Station Mode* section)
- STA P2P Client (see *Running in the ATWILC Station Mode* and *Configuring WILC device as a P2P client* sections)
- STA P2P GO (see *Running in the ATWILC Station Mode* and *Configuring WILC device as a group owner* sections)
- AP P2P Client (see *Running in the ATWILC AP Mode* and *Configuring WILC device as a P2P client* sections)
- STA AP (see *Running the ATWILC Device in Station and AP Modes Concurrently* section) **Note:** Use Wlan0 and p2p0 interfaces to run the ATWILC device concurrently.

## Running ATWILC

#### 5.8.1 Running the ATWILC Device in Station and AP Modes Concurrently

The following section describes the configuration steps to run the ATWILC device in Station (STA) and AP modes, concurrently.

1. Initialize the ATWILC1000 and ATWILC3000 driver module, using the following command:

```
Welcome to Buildroot
buildroot login: root
[root@buildroot ~]# modprobe wilc-sdio
wilc_sdio: module is from the staging directory, the quality is unknown, you have been
warned.
linux_sdio_probe init_power =0
wilc_sdio_mmc0:0001:1: Driver Initializing success
```

2. Start the WPA Supplicant service and execute wpa supplicant, using the following command:

```
# wpa_supplicant -Dnl80211 -iwlan0 -c/etc/wilc_wpa_supplicant.conf &
Successfully initialized wpa_supplicant
rfkill: Cannot open RFKILL control dev
wilc_sdio mmc0:0001:1 wlan0: Detect chip WILC3000
wilc_sdio mmc0:0001:1 wlan0: loading firmware wilc3000_wifi_firmware.bin
wilc_gnrl_async_info_received
wilc sdio mmc0:0001:1 wlan0: WILC Firmware Ver = WILC WIFI FW REL 15 00 Build: 8719
```

3. Connect to the Access Point, using the following command:

```
#wpa_cli -p/var/run/wpa_supplicant ap_scan 1
#wpa_cli -p/var/run/wpa_supplicant add_network
#wpa_cli -p/var/run/wpa_supplicant set_network 0 ssid ``User_AP"'
#wpa_cli -p/var/run/wpa_supplicant set_network 0 key_mgmt NONE
#wpa_cli -p/var/run/wpa_supplicant set_network 0 wep_tx_keyidx 0
#wpa_cli -p/var/run/wpa_supplicant set_network 0 auth_alg SHARED
#wpa_cli -p/var/run/wpa_supplicant select_network 0
```

#### 4. Run the DHCP service.

If the IP address can be allocated from the AP automatically, start the DHCP client using the following command:

#dhcpcd wlan0 &

5. Ping the **User AP** to check the connection, using the following command:

# ping 192.168.0.1

6. Run the hostapd as user's configuration.

```
# hostapd /etc/wilc_hostapd_open.conf -B &
Configuration file: /etc/wilc_hostapd_open.conf
rfkill: Cannot open RFKILL control device
wilc_sdio mmc0:0001:1 wlan0: Detect chip WILC3000
wilc_sdio mmc0:0001:1 wlan0: loading firmware wilc3000_wifi_firmware.bin
wilc_gnrl_async_info_received
wilc_sdio mmc0:0001:1 wlan0: WILC Firmware Ver = WILC_WIFI_FW_REL_15_00 Build: 8719
Using interface wlan0 with hwaddr fa:f0:05:f6:56:6a and ssid "wilc_SoftAP"
wilc_gnrl_async_info_received
wilc_sdio mmc0:0001:1 wlan0: there is no current Connect Request
wlan0: interface state UNINITIALIZED->ENABLED
wlan0: AP-ENABLED
```

- 7. Run the DHCP Server to allocate IP to client.
  - Set the IP of AP; #ifconfig p2p0 192.168.0.1
  - Start the DHCP server; #/etc/init.d/S80dhcp-server start The user can connect the PC or smartphone to the ATWILC1000 AP.

### 5.9 Powersave

#### 5.9.1 Wi-Fi Powersave

To enable Wi-Fi powersave, use iw tool.

```
$ iw dev wlan0 set power_save on
```

Note: The Powersave mode is disabled by default for AP and P2P mode.

#### 5.9.2 BLE Powersave

To use BLE powersave, UART flow control should be enabled, to hold the host back from sending new commands to the ATWILC3000 BLE controller when it is in Sleep mode.

This can be done using the Update UART Parameters vendor specific HCI command to enable flow control on ATWILC3000, then update the host's UART configuration to enable flow control. Also, the host application should allow the ATWILC3000 BLE controller to enter powersave, by setting the host's UART Tx line low, entering a Break mode. Before starting any HCI communication, the application should get the host's UART out of the Break mode, then proceed with sending the HCI commands to the ATWILC3000.

When ATWILC3000 is in Powersave mode, it will set the UART RTS line high to hold back the host from sending any additional HCI commands. Once the host UART Tx line is back high, ATWILC3000 will go out of Powersave mode, but will not be fully active instantly. After ATWILC3000 is up and ready to receive more HCI commands, it will set the UART RTS line low, and the host will be able to send more HCI commands.

This is illustrated in the following figure:



1. Yellow: UART Rx (ATWILC3000 perspective) 2. Blue: UART Tx 3. Purple: UART RTS 4. Green: ATWILC3000 Ready

To control the Break mode, IOCTL can be used as follows:

```
int main(int argc,char *argv[])
{
    int fd, serial;
    fd = open("/dev/ttyS1", O_RDWR);
    if(atoi(argv[1])==1) {
        printf("assert on %d\n",fd);
        ioctl(fd, TIOCCBRK, 0);
    } else if(atoi(argv[1])==0) {
        printf("deassert on %d\n",fd);
        ioctl(fd, TIOCSBRK, 0);
    }
    close(fd);
}
```

An example of such application is available on the reference image under etc/uart\_brk\_ioctl. To enable powersave, the following commands can be used:

```
# modprobe wilc-sdio.ko
# echo BT_POWER_UP > /dev/wilc_bt
# echo BT_DOWNLOAD_FW > /dev/wilc_bt
# hciattach ttyS1 any 115200 noflow
# hciconfig hci0 up
# hcitool cmd 0x3F 0x0053 00 C2 01 00 01
# stty -F /dev/ttyS1 crtscts
# /etc/etc/uart_brk_ioctl
```

To disable Break mode and wake up ATWILC3000, use the following command:

```
# /etc/etc/uart brk ioctl
```

# 5.10 Antenna Switching

The ATWILC devices support antenna diversity where dual antennas are connected to the chip using an external antenna switch.

Antenna switches are controlled using two input signals to select which antenna is in operation, and the user uses two different configurations with respect to the control GPIOs:

- 1. Dual GPIO two different ATWILC device GPIOs are used to control each of the antenna switch's control lines.
- Single GPIO a single ATWILC device GPIO is used to control one of the switch's control lines, and its inverse is connected to the other control line. This configuration requires an external inverter. The antenna selection algorithm evaluates the average RSSI every second, and based on that, it determines if it needs to switch the antenna.

The average RSSI is calculated based on the RSSI read while receiving each packet. If the average RSSI is below threshold, it switches to the other antenna and sets a new threshold to the average RSSI of the abandoned antenna. To avoid unnecessary switching, the antenna switching happens only when the RSSI is below -30dBm, and has a margin of 1dBm to avoid hysteresis.

Sysfs entries can be used to configure the ATWILC device driver for the Antenna Diversity mode, and the GPIOs that are used to control the antenna switch at run time.

### 5.10.1 Antenna Switch GPIO Control

Sysfs entry <code>/sys/wilc/ant\_swtch\_mode</code> can be used as follows to configure the GPIOs used to control the antenna switch:

```
# echo mode > /sys/wilc/ant_swtch_mode
```

where, mode = 1 for Single Antenna , mode = 2 for Dual Antenna and 0 - to Disable diversity.

For WILC1000 valid GPIOs are 0, 1, 4 and 6, and for WILC3000 valid GPIOs are 0, 3, 4, 17, 18, 19 and 20.

### 5.10.2 GPIOs

To configure the GPIOs that are connected to the antenna switch, sysfs entry /sys/wilc/antenna1 and /sys/wilc/antenna2 can be used as follows.

# echo GPIO\_NUM > /sys/wilc/antenna1 ( for single antenna switch)
# echo GPIO\_NUM > /sys/wilc/antenna2 ( for dual antenna switch)

where, GPIO\_NUM is any valid GPIO for antenna diversity.

Valid GPIOs for the ATWILC1000 are 0, 1, 4 and 6.

Valid GPIOs for the ATWILC3000 are 3, 4, 17, 18, 19 and 20.

### 5.10.3 Antenna Selection

The antenna used can be selected using the iw tool to either select Fixed Manual mode (antenna1 or antenna2) or automatic switching according to the antenna performance as follows:

• Set the Antenna 1, using the following command:

iw phy phy3 set antenna 1 1

• Set the Antenna 2, using the following command:

iw phy phy3 set antenna 2 2

• Enable Automatic switching, using the following command:

iw phy phy3 set antenna 3 3

**Note:** Since WILC exposes two phy devices, both devices can be used to set the antenna selection, but the same antenna selection is applied to both the devices. Also, before setting the antenna selection, the antenna switch control GPIOs should be configured.

In Manual modes, the GPIOs is set according to the following tables.

#### Table 5-4. Single Mode

Antenna Selected	GPIO1 Value
Antenna 1	1
Antenna 2	0

### Table 5-5. Dual Mode

Antenna Selected	GPIO1 Value	GPIO2 Value
Antenna 1	1	0
Antenna 2	0	1

### 5.11 Debug Logs

The ATWILC driver inherits the debug logs levels from Linux. To change the system's debug level, use one of the following methods:

#echo "7" > /proc/sys/kernel/printk

where "7" is the highest desired log level

or

# dmesg -n 7

To change the default level while building the kernel, change the following line in kernel\_src/

include/linux/printk.h

#define CONSOLE LOGLEVEL DEFAULT 7

ATWILC driver also uses debugfs to allow the user to control which code regions to enable or disable logs for.

To change it, the user has to first mount the debugfs:

# mount -t debugfs nodev /sys/kernel/debug

Then echo a number that represents a bit field of the regions that the user wants to enable logs from. The bit field is defined as follows:

BIT 0: GENERIC BIT 1: HOSTAPD BIT 2: HOSTINF BIT 3: CORECONFIG BIT 4: CFG80211 BIT 5: INT BIT 6: TX BIT 7: RX BIT 7: RX BIT 8: TCP BIT 9: INIT BIT 10: PWRDEV

## 5.12 Miscellaneous Linux Topics

This section provides additional information on Linux topics.

#### 5.12.1 Host Suspend/Resume Mechanism

Upon suspending, Linux version 4.9 disconnects the Access Point. To maintain the connection after suspending, modify the Linux code by removing the following code from the \net\wireless\sysfs.c file.

The following is the sample of the \net\wireless\sysfs.c file:

```
static int wiphy_suspend(struct device *dev, pm_message_t state)
{
    struct cfg80211_registered_device *rdev = dev_to_rdev(dev);
    int ret = 0;
    rdev->suspend_at = get_seconds();
rtnl_lock();
```

# ATWILC1000/ATWILC3000 Running ATWILC

```
if (rdev->wiphy.registered) {
    //Prevent disconnecting from connected AP's on suspension
    //if (!rdev->wiphy.wowlan_config)
        //cfg80211_leave_all(rdev);
        if (rdev->ops->suspend)
            ret = rdev_suspend(rdev, rdev->wiphy.wowlan_config);
        if (ret == 1) {
            /* Driver refuse to configure wowlan */
            cfg80211_leave_all(rdev);
            ret = rdev_suspend(rdev, NULL);
        }
    }
    rtnl_unlock();
    return ret;
}
```

The user can configure Linux in Suspend mode, using mem string in the /sys/power/state path. For more information, see https://www.kernel.org/doc/Documentation/power/interface.txt.

The controller then wakes up the host on certain wake-up on wireless LAN triggers that can be configured using the iw tool. The controller then asserts a wake-up signal on a dedicated wake-up General Purpose Input/output (GPIO) pin on the host board which is connected to the IRQ pin on ATWILC device board.

The ATWILC only supports the ANY option in the Wake on Wireless (WoW) mode from the set of allowed wake-up triggers. The host wakes up the ATWILC device upon receiving any type of packets from the connected access point if the triggers are set by the user. If it is not set by the user, the controller must not wake up the host.

To configure the host wake-up triggers as ANY, use the following any command argument:

#iw phy0 wowlan enable any

Where phy0 resembles wireless hardware interface name, and any is the required trigger.

To disable all the triggers, use the disable argument as shown in the following command:

#iw phy0 wowlan disable

To show the configured triggers, use the show argument as shown in the following command:

#iw phy0 wowlan show

To configure the host into Suspend mode, use the following command:

#echo mem > /sys/power/state

#### 5.12.2 Set Transmit Power

The user can control the Tx power of ATWILC1000 or ATWILC3000 using the iw tool with the following command line arguments.

\$ iw dev wlan0 set txpower fixed x

Where x is the desired Tx level.

The supported levels are 0, 3, 6, 9, 12, 15, and 18.

**Note:** If the input Tx power value is other than the mentioned supported levels, the x value is automatically set to the first greater value.

#### 5.12.3 Scan

To scan for the available APs, use the \$ wpa cli scan command.

#### 5.12.4 Get Scan Results

To get a list of identified APs with associated attributes such as bssid, frequency, Received Signal Strength Indicator (RSSI), encryption and Service Set Identifier (SSID), use the following command:

```
$ wpa cli scan results
Selected interface 'wlan0'
bssid / frequency / signal level / flags / ssid
                        2437 -54 [ESS]
2462 -54 [WPA2-F
02:1a:11:f5:56:81
                                                AndroidAP
68:7f:74:c7:4e:d9
                                -54
                                        [WPA2-PSK-CCMP] [WPS] [ESS]
                        2462
                                                                         тот 58
                                        [WPA-PSK-CCMP+TKIP][WPA2-PSK-CCMP+TKIP][ESS]
d8:fe:e3:03:4e:30
                        2422
                                -54
                                                                                         dlink-
enterprise
00:0c:43:44:0a:b4
                        2437
                                -51
                                        [ESS]
                                                RT2880 AP
```

#### 5.12.5 Save Network Information

To avoid the loss of network information after reboot, use the \$ wpa\_cli save\_config command.

#### 5.12.6 Load Network Information

To get the saved network information after reboot, use the \$ wpa cli list networks command.

#### 5.12.7 Get Current Network Information

To get the connected interface information of the network, which includes RSSI, channel, encryption, and so on, use the following command:

```
$ iwconfig wlan0
DBG [WILC_WFI_get_tx_power: 3418]Got tx power 18
wlan0 IEEE 802.1lbgn ESSID:"AndroidAP"
Mode:Managed Frequency:2.437 GHz Access Point: 02:1A:11:F5:56:81
Bit Rate=0 kb/s Tx-Power=18 dBm
Retry short limit:7 RTS thr:off Fragment thr:off
Encryption key:off
Power Management:on
Link Quality=49/70 Signal level=-61 dBm
Rx invalid nwid:0 Rx invalid crypt:0 Rx invalid frag:0
Tx excessive retries:0 Invalid misc:0 Missed beacon:0
```

### 5.12.8 Get Current Regulatory Domain

To get a list of identified APs with associated attributes such as bssid, frequency, RSSI, encryption, and SSID, use the following command:

```
$ iw reg get
country EG: DFS-UNSET
        (2402 - 2482 @ 40), (N/A, 20)
        (5170 - 5250 @ 80), (N/A, 20)
        (5250 - 5330 @ 80), (N/A, 20), DFSiwconfig wlan0
```

#### 5.12.9 Set Current Regulatory Domain

To get a list of identified APs with associated attributes such as like bssid, frequency, RSSI, encryption and SSID, use the following command:

```
$ iw reg set US
cfg80211: Calling CRDA for country: US
[root@buildroot ~]# cfg80211: Regulatory domain changed to country: US
cfg80211: DFS Master region: unset
cfg80211: (start_freq - end freq @ bandwidth), (max_antenna_gain, max_eirp), (dfs_cac_time)
cfg80211: (240200 KHz - 2472000 KHz @ 40000 KHz), (N/A, 3000 mBm), (N/A)
cfg80211: (5170000 KHz - 5250000 KHz @ 80000 KHz), (N/A, 1700 mBm), (N/A)
```

# ATWILC1000/ATWILC3000

## **Running ATWILC**

cfg80211:(5250000 KHz - 5330000 KHz @ 80000 KHz), (N/A, 2300 mBm), (0 s)cfg80211:(5735000 KHz - 5835000 KHz @ 80000 KHz), (N/A, 3000 mBm), (N/A)cfg80211:(57240000 KHz - 63720000 KHz @ 2160000 KHz), (N/A, 4000 mBm), (N/A)

To change the default regulatory domain that Linux uses at startup, the user must edit the configuration file that was passed while starting the wpa cli using the vi tool. The configuration is as follows:

```
$ vi /etc/wilc_wpa_supplicant.conf
ctrl_interface=/var/run/wpa_supplicant
update_config=1
country=US
network={
    ssid="AndroidAP"
    key_mgmt=NONE
}
```

## 5.13 Running ATWILC3000 in Bluetooth Mode

Use the following commands to use BLE after loading the wilc-sdio.ko modules.

When WILC3000 initializes, it creates a node at /dev/wilc\_bt, which can be used to write the following commands:

- BT\_POWER\_UP
- BT\_DOWNLOAD\_FW
- BT FW CHIP WAKEUP
- BT\_FW\_CHIP\_ALLOW\_SLEEP
- BT POWER DOWN

### 5.13.1 BT\_POWER\_UP

The following command powers up the chip, and indicates that the BT requires the chip to be ON.

```
$ echo BT POWER UP > /dev/wilc bt
```

### 5.13.2 BT\_DOWNLOAD\_FW

The following command downloads the BT firmware using SDIO.

\$ echo BT\_DOWNLOAD\_FW > /dev/wilc\_bt

#### 5.13.3 BT\_FW\_CHIP\_WAKEUP

The following command prevents the chip from sleeping.

```
$ echo BT_FW_CHIP_WAKEUP > /dev/wilc_bt
```

This command is used before downloading the firmware using Universal Asynchronous Receiver/ Transmitter (UART). Otherwise, the chip may go to Sleep mode when the stack is downloading the BT firmware.

#### 5.13.4 BT\_FW\_CHIP\_ALLOW\_SLEEP

The following command specifies that the at\_pwr\_dev module does not require the chip to be awake. The user must use this command after downloading and starting the BT firmware using UART, allowing the BT and Wi-Fi firmwares to take sleep or wake decisions.

```
$ echo BT_FW_CHIP_ALLOW_SLEEP > /dev/wilc_bt
```

#### 5.13.5 BT\_POWER\_DOWN

The following command is used to chip down the power when the BT is not in use.

```
$ echo BT POWER DOWN > /dev/wilc bt
```

The chip cannot be powered-down using the BT\_POWER\_DOWN command, if Wi-Fi is active. However, using BT\_POWER\_UP and BT\_POWER\_DOWN in the correct sequence the user can power on and off the chip successfully.

### 5.13.6 Attaching UART for Bluetooth

The ATWILC3000 Bluetooth driver provides the UART interface and is connected via a Teletypewriter (TTY) device. It is connected to the BlueZ stack.

The following command is used to attach the device. Ensure that the /dev/ttyS1 folder is available on the target platform. The user must set the Bluetooth firmware baud rate at 115200 and should enable noflow control.

\$ hciattach ttyS1 any 115200 noflow

Ensure that the Host Control Interface (HCI) is created.

```
$ hciconfig -a
hci0: Type: BR/EDR Bus: UART
BD Address: AB:89:67:45:23:01 ACL MTU: 1021:9 SCO MTU: 255:4
DOWN
RX bytes:574 acl:0 sco:0 events:27 errors:0
TX bytes:411 acl:0 sco:0 commands:27 errors:0
Features: 0xff 0xff 0xcd 0xfe 0xdb 0xff 0x7b 0x87
Packet type: DM1 DM3 DM5 DH1 DH3 DH5 HV1 HV2 HV3
Link policy: RSWITCH HOLD SNIFF PARK
Link mode: SLAVE ACCEPT
```

### 5.13.7 Enabling the Bluetooth Interface

Enable the ATWILC3000 Bluetooth HCI interface, using the following command.

\$ hciconfig hci0 up

#### 5.13.8 Run bluetoothd (Bluetooth daemon)

The user must create symbolic link for the bluetoothd as:

```
$ ln -svf /usr/libexec/bluetooth/bluetoothd /usr/sbin
```

Start the Bluetooth daemon in background using the \$ bluetoothd -n & command.

#### 5.13.9 Scanning for Devices

The user can scan for the neighboring networks using the \$ scan on command. This command displays a list of networks showing the Bluetooth address (BD\_ADDR) and name when the scan is complete.

Start the bluetoothctl using the \$ bluetoothctl command, which can be used to scan and connect.

The following is a sample when the scan is started:

```
$ scan on
Scanning ...
60:6C:66:A4:29:63 D247-PC
60:03:08:89:93:E7 damiank-mbp1
E0:06:E6:BE:A8:FA APDN194
78:DD:08:B2:91:C9 ALEX-PC
```

#### 5.13.10 Connecting to a Device

It is recommended to use the DBUS interface to connect to a device that is found during scanning.

Use the connect command to connect to the device with the specified Bluetooth address.

For example, to connect to the Bluetooth address 00:02:3C:3A:95:6F, use the following command:

\$ connect 00:02:3C:3A:95:6F

### 5.13.11 BLE Peripheral Mode Example For BlueZ 5.28 and Earlier

BlueZ can be used to run in BLE Peripheral mode using the Low Energy Advertise command (leadv). The Bluetooth Daemon (bluetoothd) is also used to provide time profile using the following commands:

```
[root@buildroot ~]# modprobe wilc-sdio
wilc_sdio: module is from the staging directory, the quality is unknown, you have been warned.
linux_sdio_probe init_power =0
wilc sdio mmc0:0001:1: Driver Initializing success
[root@buildroot ~] # mmc0: card 0001 removed
mmc0: new high speed SDIO card at address 0001
linux sdio probe init power =1
wilc_sdio mmc0:0001:1: Driver Initializing success
# echo BT SDIO INIT > /dev/wilc bt
[root@buildroot ~] # echo BT POWER UP > /dev/wilc bt
[root@buildroot ~]# echo BT_FW_CHIP_WAKEUP > /dev/wilc_bt
[root@buildroot ~]# echo BT_DOWNLOAD_FW > /dev/wilc_bt
[root@buildroot ~] # echo BT FW CHIP ALLOW SLEEP > /dev/wilc bt
[root@buildroot ~] # hciattach ttyS1 any 115200 noflow
atmel usart fc010000.serial: using dma0chan10 for rx DMA transfers
atmel_usart fc010000.serial: using dma0chan11 for tx DMA transfers
Device setup complete
[root@buildroot ~] # hciconfig hci0 up
[root@buildroot ~] # g serial gadget: high-speed config #2: CDC ACM config
ln -svf /usr/libexec/bluetooth/bluetoothd /usr/sbin
'/usr/sbin/bluetoothd' -> '/usr/libexec/bluetooth/bluetoothd'
[root@buildroot ~] # bluetoothd -p time -n &
[1] 845
[root@buildroot ~]# bluetoothd[845]: Bluetooth daemon 5.21
bluetoothd[845]: Starting SDP server
bluetoothd[845]: Ignoring (cli) hostname
bluetoothd[845]: Ignoring (cli) wiimote
bluetoothd[845]: Ignoring (cli) autopair
bluetoothd[845]: Ignoring (cli) policy
bluetoothd[845]: Ignoring (cli) neard
bluetoothd[845]: Ignoring (cli) sap
bluetoothd[845]: Ignoring (cli) a2dp
bluetoothd[845]: Ignoring (cli) avrcp
bluetoothd[845]: Ignoring (cli) network
bluetoothd[845]: Ignoring (cli) input
bluetoothd[845]: Ignoring (cli) hog
bluetoothd[845]: Ignoring (cli) health
bluetoothd[845]: Ignoring (cli) gatt
bluetoothd[845]: Ignoring (cli) scanparam
bluetoothd[845]: Ignoring (cli) deviceinfo
bluetoothd[845]: Ignoring (cli) alert
bluetoothd[845]: Ignoring (cli) proximity
bluetoothd[845]: Ignoring (cli) thermometer
bluetoothd[845]: Ignoring (cli) heartrate
bluetoothd[845]: Ignoring (cli) cyclingspeed
bluetoothd[845]: Failed to open RFKILL control device
bluetoothd[845]: Bluetooth management interface 1.14 initialized
[root@buildroot ~]# hciconfig -a
hci0: Type: BR/EDR Bus: UART
BD Address: F8:F0:05:F7:36:9E ACL MTU: 1021:9 SCO MTU: 255:4
UP RUNNING PSCAN
RX bytes:1257 acl:0 sco:0 events:67 errors:0
TX bytes:1381 acl:0 sco:0 commands:67 errors:0
Features: 0xff 0xff 0xcd 0xfe 0xdb 0xff 0x7b 0x87
Packet type: DM1 DM3 DM5 DH1 DH3 DH5 HV1 HV2 HV3
Link policy: RSWITCH HOLD SNIFF PARK
Link mode: SLAVE ACCEPT
```

```
Name: 'BlueZ 5.21'
Class: 0x000000
Service Classes: Unspecified
Device Class: Miscellaneous,
HCI Version: 4.0 (0x6) Revision: 0x709
LMP Version: 4.0 (0x6) Subversion: 0x709
Manufacturer: Atmel Corporation (19)
[root@buildroot ~]# hciconfig hci0 leadv
```

#### 5.13.12 BLE Peripheral Mode Example for BlueZ 5.29 and Later

Starting with blueZ 5.29 and later, the time profile is no longer supported using bluetoothd. An alternative approach is to use the btgatt-server example that is automatically built while building the blueZ package. However, it is important to note that buildroot does not install this example to the target by default, and it should be transferred manually to the host using scp or rz.

To install it automatically, the .mk file for blueZ in the buildroot system will need to be modified as follows:

- 1. Edit file buildroot/package/bluez5 utils/bluez5 utils.mk.
- 2. Add the following lines at the end of the file before \$ (eval \$ (autotools-package))

```
define BLUE25_UTILS_INSTALL GATTEXAMPLE
    $(INSTALL) -D -m 0755 $(@D)/tools/btgatt-server $(TARGET_DIR)/usr/bin/btgatt-
server
endef
BLUE25_UTILS_POST_INSTALL_TARGET_HOOKS += BLUE25_UTILS_INSTALL_GATTEXAMPLE
```

To run the example, use the following commands:

```
# modprobe wilc-sdio
wilc sdio: module is from the staging directory, the quality is unknown, you have been warned.
(unnamed net device) (uninitialized): INFO [wilc create wiphy]Registering wifi device
(unnamed net_device) (uninitialized): INFO [WILC_WFI CfgAlloc]Allocating wireless device
(unnamed net_device) (uninitialized): INFO [wilc_create_wiphy]Successful Registering
(unnamed net_device) (uninitialized): INFO [wilc_create_wiphy]Registering wifi device
(unnamed net_device) (uninitialized): INFO [WILC_WFI_CfgAlloc]Allocating wireless device
(unnamed net device) (uninitialized): INFO [wilc create wiphy]Successful Registering
wilc_sdio mmc0:0001:1: WILC got 60 for gpio_reset
wilc_sdio mmc0:0001:1: WILC got 94 for gpio_chip_en
wilc_sdio mmc0:0001:1: WILC got 91 for gpio_irq
wifi pm : 0
wifi pm : 1
wilc sdio mmc0:0001:1: Driver Initializing success
# wilc sdio mmc0:0001:1 wlan0: INFO [wilc_netdev_cleanup]Unregistering netdev d4782000
wilc_sdio mmc0:0001:1 wlan0 (unregistered): INFO [wilc_netdev_cleanup]Freeing Wiphy...
wilc_sdio mmc0:0001:1 wlan0 (unregistered): INFO [wilc_free_wiphy]Unregistering wiphy
wilc sdio mmc0:0001:1 wlan0 (unregistered): INFO [wilc free wiphy]Freeing wiphy
wilc_sdio mmc0:0001:1 wlan0 (unregistered): INFO [wilc_netdev_cleanup]Freeing netdev...
wilc_sdio mmc0:0001:1 p2p0: INFO [wilc_netdev_cleanup]Unregistering netdev d477b000
wilc sdio mmc0:0001:1 p2p0 (unregistered): INFO [wilc netdev cleanup]Freeing Wiphy...
wilc_sdio mmc0:0001:1 p2p0 (unregistered): INFO [wilc_free_wiphy]Unregistering wiphy
wilc_sdio mmc0:0001:1 p2p0 (unregistered): INFO [wilc_free_wiphy]Freeing wiphy
wilc_sdio mmc0:0001:1 p2p0 (unregistered): INFO [wilc_netdev_cleanup]Freeing netdev...
Module exit Done.
at_pwr_dev: deinit
at pwr dev: unregistered
mmc0: card 0001 removed
mmc0: new high speed SDIO card at address 0001
(unnamed net_device) (uninitialized): INFO [wilc_create_wiphy]Registering wifi device
(unnamed net_device) (uninitialized): INFO [WILC_WFI CfgAlloc]Allocating wireless device
(unnamed net_device) (uninitialized): INFO [wilc_create_wiphy]Successful Registering (unnamed net_device) (uninitialized): INFO [wilc_create_wiphy]Registering wifi device
(unnamed net_device) (uninitialized): INFO [WILC_WFI_CfgAlloc]Allocating wireless device
(unnamed net_device) (uninitialized): INFO [wilc_create_wiphy]Successful Registering
wilc sdio mmc0:0001:1: WILC got 60 for gpio reset
wilc_sdio mmc0:0001:1: WILC got 94 for gpio_chip_en
wilc_sdio mmc0:0001:1: WILC got 91 for gpio_irq
wilc sdio mmc0:0001:1: Driver Initializing success
# echo BT POWER UP > /dev/wilc bt
at_pwr_dev: open()
AT PWR: bt_power_up
```

# ATWILC1000/ATWILC3000

. . .

. .

### **Running ATWILC**

```
wilc sdio mmc0:0001:1: SDIO speed: 50000000
wilc_sdio mmc0:0001:1: chipid 003000d0
WILC POWER UP
at_pwr_dev: close()
# echo BT FW CHIPaWt pUwr dev: open()
 > /at pwwrc dtev: close()
#
# echo BT_DOWNLOAD_FW > /dev/wilc_bt
at pwr dev: open()
AT PWR: bt download fw
Bluetooth firmware: mchp/wilc3000 ble firmware.bin
Downloading BT firmware size = 58\overline{2}76 ...
Starting BT firmware
BT Start Succeeded
at_pwr_dev: close()
# echo BT_FW_CHIP_ALLOW_SLEEP > /dev/wilc_bt
at_pwr_dev: open()
at_pwr_dev: close()
# hciattach ttyS1 any 115200 noflow
atmel usart fc010000.serial: using dma0chan10 for rx DMA transfers
atmel_usart fc010000.serial: using dma0chan11 for tx DMA transfers
Device setup complete
# hciconfig hci0 up
#
# hciconfig hci0 leadv
# btgatt-server -i hci0 -s low -t public -r -v
Started listening on ATT channel. Waiting for connections
Connect from 49:0D:EA:C2:98:66
NET: Registered protocol family 38
Running GATT server
[GATT server] # att: > 0a 10 00
[GATT server] # att: ATT PDU received: 0x0a
[GATT server] # server: Read Req - handle: 0x0010
[GATT server] # att: ATT op 0x0b
[GATT server]# att: < 0b 01
[GATT server]#</pre>
```

# 6. Document Revision History

# Rev B - 06/2018

Section	Changes
Document	<ul> <li>Updated procedure for building Linux for SAMA5D4 Xplained Ultra Board</li> <li>Updated the procedure for updating ATWILC Firmware</li> <li>Added information about Powersave</li> </ul>
	<ul> <li>Added information about Powersave, Antenna Switching, and Debug Logs</li> <li>Added details about BLE Peripheral Mode example for BlueZ 5.28 and Earlier, and BlueZ 5.29 and Later</li> </ul>

### Rev A - 08/2017

Section	Changes
Document	Initial Release

# The Microchip Web Site

Microchip provides online support via our web site at http://www.microchip.com/. This web site is used as a means to make files and information easily available to customers. Accessible by using your favorite Internet browser, the web site contains the following information:

- Product Support Data sheets and errata, application notes and sample programs, design resources, user's guides and hardware support documents, latest software releases and archived software
- **General Technical Support** Frequently Asked Questions (FAQ), technical support requests, online discussion groups, Microchip consultant program member listing
- Business of Microchip Product selector and ordering guides, latest Microchip press releases, listing of seminars and events, listings of Microchip sales offices, distributors and factory representatives

# **Customer Change Notification Service**

Microchip's customer notification service helps keep customers current on Microchip products. Subscribers will receive e-mail notification whenever there are changes, updates, revisions or errata related to a specified product family or development tool of interest.

To register, access the Microchip web site at <a href="http://www.microchip.com/">http://www.microchip.com/</a>. Under "Support", click on "Customer Change Notification" and follow the registration instructions.

# **Customer Support**

Users of Microchip products can receive assistance through several channels:

- Distributor or Representative
- Local Sales Office
- Field Application Engineer (FAE)
- Technical Support

Customers should contact their distributor, representative or Field Application Engineer (FAE) for support. Local sales offices are also available to help customers. A listing of sales offices and locations is included in the back of this document.

Technical support is available through the web site at: http://www.microchip.com/support

# **Microchip Devices Code Protection Feature**

Note the following details of the code protection feature on Microchip devices:

- Microchip products meet the specification contained in their particular Microchip Data Sheet.
- Microchip believes that its family of products is one of the most secure families of its kind on the market today, when used in the intended manner and under normal conditions.
- There are dishonest and possibly illegal methods used to breach the code protection feature. All of these methods, to our knowledge, require using the Microchip products in a manner outside the operating specifications contained in Microchip's Data Sheets. Most likely, the person doing so is engaged in theft of intellectual property.
- Microchip is willing to work with the customer who is concerned about the integrity of their code.

• Neither Microchip nor any other semiconductor manufacturer can guarantee the security of their code. Code protection does not mean that we are guaranteeing the product as "unbreakable."

Code protection is constantly evolving. We at Microchip are committed to continuously improving the code protection features of our products. Attempts to break Microchip's code protection feature may be a violation of the Digital Millennium Copyright Act. If such acts allow unauthorized access to your software or other copyrighted work, you may have a right to sue for relief under that Act.

# Legal Notice

Information contained in this publication regarding device applications and the like is provided only for your convenience and may be superseded by updates. It is your responsibility to ensure that your application meets with your specifications. MICROCHIP MAKES NO REPRESENTATIONS OR WARRANTIES OF ANY KIND WHETHER EXPRESS OR IMPLIED, WRITTEN OR ORAL, STATUTORY OR OTHERWISE, RELATED TO THE INFORMATION, INCLUDING BUT NOT LIMITED TO ITS CONDITION, QUALITY, PERFORMANCE, MERCHANTABILITY OR FITNESS FOR PURPOSE. Microchip disclaims all liability arising from this information and its use. Use of Microchip devices in life support and/or safety applications is entirely at the buyer's risk, and the buyer agrees to defend, indemnify and hold harmless Microchip from any and all damages, claims, suits, or expenses resulting from such use. No licenses are conveyed, implicitly or otherwise, under any Microchip intellectual property rights unless otherwise stated.

# Trademarks

The Microchip name and logo, the Microchip logo, AnyRate, AVR, AVR logo, AVR Freaks, BeaconThings, BitCloud, CryptoMemory, CryptoRF, dsPIC, FlashFlex, flexPWR, Heldo, JukeBlox, KeeLoq, KeeLoq logo, Kleer, LANCheck, LINK MD, maXStylus, maXTouch, MediaLB, megaAVR, MOST, MOST logo, MPLAB, OptoLyzer, PIC, picoPower, PICSTART, PIC32 logo, Prochip Designer, QTouch, RightTouch, SAM-BA, SpyNIC, SST, SST Logo, SuperFlash, tinyAVR, UNI/O, and XMEGA are registered trademarks of Microchip Technology Incorporated in the U.S.A. and other countries.

ClockWorks, The Embedded Control Solutions Company, EtherSynch, Hyper Speed Control, HyperLight Load, IntelliMOS, mTouch, Precision Edge, and Quiet-Wire are registered trademarks of Microchip Technology Incorporated in the U.S.A.

Adjacent Key Suppression, AKS, Analog-for-the-Digital Age, Any Capacitor, AnyIn, AnyOut, BodyCom, chipKIT, chipKIT logo, CodeGuard, CryptoAuthentication, CryptoCompanion, CryptoController, dsPICDEM, dsPICDEM.net, Dynamic Average Matching, DAM, ECAN, EtherGREEN, In-Circuit Serial Programming, ICSP, Inter-Chip Connectivity, JitterBlocker, KleerNet, KleerNet Iogo, Mindi, MiWi, motorBench, MPASM, MPF, MPLAB Certified Iogo, MPLIB, MPLINK, MultiTRAK, NetDetach, Omniscient Code Generation, PICDEM, PICDEM.net, PICkit, PICtail, PureSilicon, QMatrix, RightTouch Iogo, REAL ICE, Ripple Blocker, SAM-ICE, Serial Quad I/O, SMART-I.S., SQI, SuperSwitcher, SuperSwitcher II, Total Endurance, TSHARC, USBCheck, VariSense, ViewSpan, WiperLock, Wireless DNA, and ZENA are trademarks of Microchip Technology Incorporated in the U.S.A. and other countries.

SQTP is a service mark of Microchip Technology Incorporated in the U.S.A.

Silicon Storage Technology is a registered trademark of Microchip Technology Inc. in other countries.

GestIC is a registered trademark of Microchip Technology Germany II GmbH & Co. KG, a subsidiary of Microchip Technology Inc., in other countries.

All other trademarks mentioned herein are property of their respective companies.

© 2018, Microchip Technology Incorporated, Printed in the U.S.A., All Rights Reserved.

ISBN: 978-1-5224-3249-4

# Quality Management System Certified by DNV

### ISO/TS 16949

Microchip received ISO/TS-16949:2009 certification for its worldwide headquarters, design and wafer fabrication facilities in Chandler and Tempe, Arizona; Gresham, Oregon and design centers in California and India. The Company's quality system processes and procedures are for its PIC<sup>®</sup> MCUs and dsPIC<sup>®</sup> DSCs, KEELOQ<sup>®</sup> code hopping devices, Serial EEPROMs, microperipherals, nonvolatile memory and analog products. In addition, Microchip's quality system for the design and manufacture of development systems is ISO 9001:2000 certified.



# **Worldwide Sales and Service**

AMERICAS	ASIA/PACIFIC	ASIA/PACIFIC	EUROPE
Corporate Office	Australia - Sydney	India - Bangalore	Austria - Wels
2355 West Chandler Blvd.	Tel: 61-2-9868-6733	Tel: 91-80-3090-4444	Tel: 43-7242-2244-39
Chandler, AZ 85224-6199	China - Beijing	India - New Delhi	Fax: 43-7242-2244-393
Tel: 480-792-7200	Tel: 86-10-8569-7000	Tel: 91-11-4160-8631	Denmark - Copenhagen
Fax: 480-792-7277	China - Chengdu	India - Pune	Tel: 45-4450-2828
Technical Support:	Tel: 86-28-8665-5511	Tel: 91-20-4121-0141	Fax: 45-4485-2829
http://www.microchip.com/	China - Chongging	Japan - Osaka	Finland - Espoo
support	Tel: 86-23-8980-9588	Tel: 81-6-6152-7160	Tel: 358-9-4520-820
Web Address:	China - Dongguan	Japan - Tokyo	France - Paris
www.microchip.com	Tel: 86-769-8702-9880	Tel: 81-3-6880- 3770	Tel: 33-1-69-53-63-20
Atlanta	China - Guangzhou	Korea - Daegu	Fax: 33-1-69-30-90-79
Duluth, GA	Tel: 86-20-8755-8029	Tel: 82-53-744-4301	Germany - Garching
Tel: 678-957-9614	China - Hangzhou	Korea - Seoul	Tel: 49-8931-9700
- ax: 678-957-1455	Tel: 86-571-8792-8115	Tel: 82-2-554-7200	Germany - Haan
Austin, TX	China - Hong Kong SAR	Malaysia - Kuala Lumpur	Tel: 49-2129-3766400
Tel: 512-257-3370	Tel: 852-2943-5100	Tel: 60-3-7651-7906	Germany - Heilbronn
Boston	China - Nanjing	Malaysia - Penang	Tel: 49-7131-67-3636
Westborough, MA	Tel: 86-25-8473-2460	Tel: 60-4-227-8870	Germany - Karlsruhe
Tel: 774-760-0087	China - Qingdao	Philippines - Manila	Tel: 49-721-625370
Fax: 774-760-0088	Tel: 86-532-8502-7355	Tel: 63-2-634-9065	Germany - Munich
Chicago	China - Shanghai	Singapore	Tel: 49-89-627-144-0
tasca, IL	Tel: 86-21-3326-8000	Tel: 65-6334-8870	Fax: 49-89-627-144-44
Tel: 630-285-0071	China - Shenyang	Taiwan - Hsin Chu	Germany - Rosenheim
Fax: 630-285-0075	Tel: 86-24-2334-2829	Tel: 886-3-577-8366	Tel: 49-8031-354-560
Dallas	China - Shenzhen	Taiwan - Kaohsiung	Israel - Ra'anana
Addison, TX	Tel: 86-755-8864-2200	Tel: 886-7-213-7830	Tel: 972-9-744-7705
Tel: 972-818-7423		Taiwan - Taipei	Italy - Milan
Fax: 972-818-2924	China - Suzhou Tel: 86-186-6233-1526	Tel: 886-2-2508-8600	Tel: 39-0331-742611
Detroit	China - Wuhan	Thailand - Bangkok	Fax: 39-0331-466781
Novi, MI	Tel: 86-27-5980-5300	Tel: 66-2-694-1351	Italy - Padova
Tel: 248-848-4000	China - Xian	Vietnam - Ho Chi Minh	Tel: 39-049-7625286
Houston, TX	Tel: 86-29-8833-7252	Tel: 84-28-5448-2100	Netherlands - Drunen
Tel: 281-894-5983	China - Xiamen	Tel. 64-26-5446-2100	Tel: 31-416-690399
	Tel: 86-592-2388138		
Indianapolis	China - Zhuhai		Fax: 31-416-690340
Noblesville, IN	Tel: 86-756-3210040		Norway - Trondheim Tel: 47-7289-7561
Tel: 317-773-8323			
Fax: 317-773-5453 Tel: 317-536-2380			Poland - Warsaw
			Tel: 48-22-3325737
Los Angeles			Romania - Bucharest Tel: 40-21-407-87-50
Mission Viejo, CA			
Tel: 949-462-9523			<b>Spain - Madrid</b> Tel: 34-91-708-08-90
Fax: 949-462-9608			
Tel: 951-273-7800			Fax: 34-91-708-08-91
Raleigh, NC			Sweden - Gothenberg
Tel: 919-844-7510			Tel: 46-31-704-60-40
New York, NY			Sweden - Stockholm
Tel: 631-435-6000			Tel: 46-8-5090-4654
San Jose, CA			UK - Wokingham
Tel: 408-735-9110			Tel: 44-118-921-5800
Tel: 408-436-4270			Fax: 44-118-921-5820
Canada - Toronto			

Tel: 905-695-1980 Fax: 905-695-2078