GALAXY
SEMICONDUCTOR INTELLIGENCE

Key Features
Standard and custom reports:

- **GLOBAL INFORMATION**
  Tester, lot, operator information

- **YIELD INFORMATION**
  Good parts, failing parts, parts retested

- **TEST SUMMARY**
  Cp, Cpk, mean, sigma, skew, fail count, etc.

- **TEST HISTOGRAM**
  Quickly view your test distribution

- **PARAMETRIC TEST WAFER MAP**
  Analyze the test gradient on your wafer to investigate process problems

- **WAFER MAP**
  See all dice tested in production, binning, time info

- **FLEXIBLE INPUT FORMAT**
  Supports a broad range of different tester platforms and data formats, including STDF, GDF, ATDF, WAT, PCM, CSV, and 60+ others

- **PARAMETRIC TREND CHARTS**
  See how test results evolve over time in production

- **PARETO REPORTS**
  Cpk of tests, failures, binning

- **CORRELATION REPORTS**
  Conduct gage R/R studies

Examinator-Pro™ Features
Includes all Examinator features plus:

- Wafer stacking and comparison
- Flat file or RDB–based analysis
- Multi-variable characterization wizard
- Reliability and shift analysis
- Serves as client for Yield-Man

Examinator™
The fastest and easiest way to analyze IC test data.

Examinator (the combination of EXAMINe + translATOR) offers product and test engineers everything they need to characterize new ICs and test programs, quickly ramp up production at OSATs and identify and diagnose yield excursions when they occur. Examinator turns standard test data files like STDF, PCM and 60+ other formats into comprehensive reports, and enables you to drill down into the data interactively for root-cause analysis.

The "Pro" version offers all of the capabilities of Examinator plus some powerful new productivity features, including shift analysis for accelerated life testing, multi-variable characterization wizard, and fast, flexible queries on huge datasets stored in a Test Data Repository (based on MySQL or Oracle). Examinator-Pro also serves as a client for Yield-Man, so if you'd like to automate the yield management process at some point in the future, you'll preserve your investment and utilize the same user interface.

Examinator is incredibly easy to use, fast, and requires no special training. You can download the free trial version and be up and running in minutes, analyzing your test data. The majority of our 3500+ users are product, test and process engineers from fabless, IDM, OSAT and ATE companies. Given the huge amount of data generated by the testing process and the time pressure to resolve issues quickly, rapid data analysis is critical. And that's what Examinator delivers.

Examinator Applications
Examinator is used for a broad range of applications in engineering and production processes, including:

- PVT corner case analysis (Pro)
- Test program prove-in/limit tuning
- Tester, load-board correlation studies
- Test time optimization
- Root cause analysis for yield excursions
- Subcontractor qualification

Examinator™

Product Configurations

Examinator™
Configured primarily for file-based device and test program characterization. Suitable for analysis of a small number of data files (10's of MBs of data).

Examinator-Pro™
Configured for both file-based and TDR-based analysis, including advanced characterization and production yield analysis. Suitable for analysis of large data sets and multiple lots (GB's to TB's of data).

System Requirements

PC Hardware
- CPU: Intel or AMD dual-core x86 or IA-64, 1.2 GHz min
- Memory: 4 GB recommended, 2 GB minimum
- Disk space: 50 MB for application plus additional space for data files

Operating System
- Windows 7 (64 bit recommended), Vista, XP
- Linux: Red Hat Enterprise 6.2-64 bit or higher, Fedora 12-64 bit or higher. Other Linux variants are also supported- consult Galaxy for details
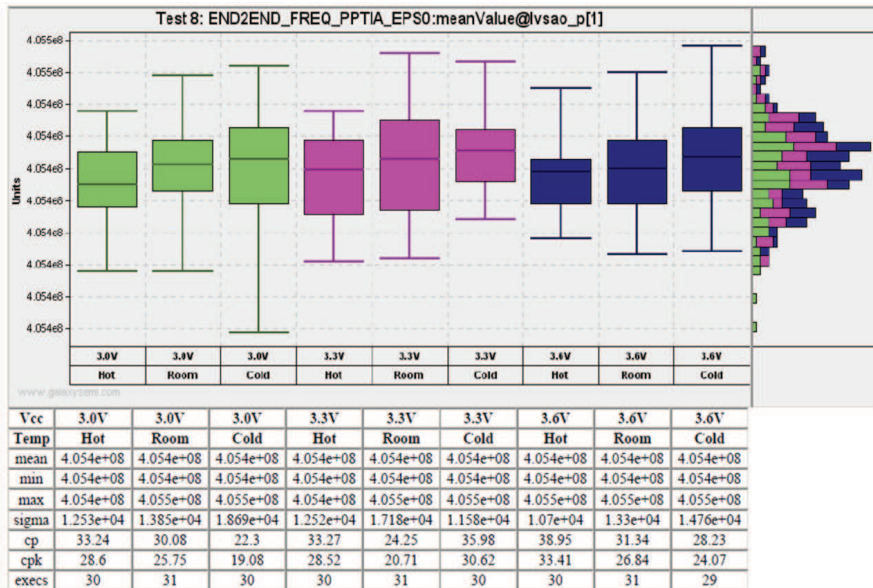
Customer Testimonials

"It is lightning fast"

"You have the most responsive support I've ever seen!"

"What used to take me up to 10 hours, I was able to gather, analyze, and put together in 20 minutes, just before the weekly product review meeting".

"Examinator's analysis is really fast; we use it for quick correlation checks and parameter drift analysis, especially during qualification trials. We like the fact that it does not require any special training..."

Test 8: END2END_FREQ_PPTIA_EPS0:meanValue@lvsao_p[1]

Vcc	3.0V	3.0V	3.0V	3.3V	3.3V	3.3V	3.6V	3.6V	3.6V
Temp	Hot	Room	Cold	Hot	Room	Cold	Hot	Room	Cold
mean	4.054e+08	4.054e+08	4.054e+08	4.054e+08	4.054e+08	4.054e+08	4.054e+08	4.054e+08	4.054e+08
min	4.054e+08	4.054e+08	4.054e+08	4.054e+08	4.054e+08	4.054e+08	4.054e+08	4.054e+08	4.054e+08
max	4.054e+08	4.055e+08	4.055e+08	4.054e+08	4.055e+08	4.055e+08	4.055e+08	4.055e+08	4.055e+08
sigma	1.253e+04	1.385e+04	1.869e+04	1.252e+04	1.718e+04	1.158e+04	1.07e+04	1.33e+04	1.476e+04
cp	33.24	30.08	22.3	33.27	24.25	35.98	38.95	31.34	28.23
cpk	28.6	25.75	19.08	28.52	20.71	30.62	33.41	26.84	24.07
execs	30	31	30	30	31	30	30	31	29

New multi-variable characterization wizard creates intuitive composite reports in minutes, showing the impact of temperature, voltage, process and any other variables.

Advanced Features (Examinator-Pro with TDR option)

Examinator-Pro when coupled with the Test Data Repository (TDR) option also includes the following new features that enhance your productivity:

- Test Data Repository-based analysis: provides fast, flexible analysis of large data sets
- Multi-variable Characterization Wizard: dramatically simplifies device characterization and corner-case/split-lot analysis across any number of conditions
- Report Center: creates consolidated reports including test and retest data, and makes it easy to split data by lot, sub-lot, tester, site, load-board, etc.
- Die Trace: traces serialized parts from final test back to wafer map
- Genealogy: correlates wafer sort parameters back to fab PCM data

Benefits

Thousands of users attest to the value of Examinator in helping them get their job done faster and allowing more time for high-value activities. And since it acts as a common user interface for all Galaxy products, using Examinator helps prepare you for automated yield management with Yield-Man and automated outlier removal with PAT-Man.

It's no wonder that Examinator has become a de facto industry standard and is used internally by all the major ATE companies and many OSATs.

TRY IT!
Simply download the free trial version at www.galaxysemi.com
You'll be up and running in minutes, analyzing your own data.

About Galaxy
Galaxy Semiconductor Solutions is a software company that builds and markets semiconductor intelligence tools that help chip companies improve device quality, reduce costs and boost engineering productivity. Its products are used by over 3500 engineers worldwide and by all the leading ATE companies. Galaxy is well known for offering the best user experience with tools that are easy to use and offer a very fast time to benefit.

GALAXY

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